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A Technical Survey on Control Strategies of Active Filter for Harmonic Suppression

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Abstract

In modern era, the tradition of non-linear loads in Electrical power system has sparked the research in power quality (PQ) concern like voltage flicker, voltage sag and swell, harmonics, etc., Among all the other concerns, one of the the majority significant of PQ problem is Harmonics .Active Power Filter (APF) is one of most challenging filter for the mitigation of harmonics. The heart of the Active power Filter is the controller part. In this study, a wide-ranging review of various controllers have been projected ,Which mitigate harmonics, reactive power and unbalanced load currents originating from load side, and also the advantages and disadvantages of presented techniques are presented. The study also helps the researchers to select the optimum control strategies and power circuit configuration for APF applications.

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1. Introduction

To reduce the harmonics conventionally passive L–C filters were used and also capacitors were employed to improve the power factor of the ac loads .But the passive filters have several drawbacks like fixed compensation, large size and resonance problem. To mitigate the harmonics problem [6][7], many research work development are developed on the active power (APF) filters or active power line conditioners [1]-[5] .Active filters appear to be a viable solution for power quality conditioning [7]-[9].

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With the emergence of fast computing devices, control strategies [8] for active filters are continually changing aiming at near perfect compensation. This paper presents a review of the state-of-the-art control techniques in active filters and reactive power

2. Control techniques for active power filters

Advancement in Microelectronics has motivated new directions for APF design starting from the use of analog and digital components to microprocessors, microcontrollers, digital signal processors (DSP's)[27] and FPGA[34] implementation. From fig 1 it shows that control part plays important role in filtering. Further, these developments have made it possible to use different control algorithm, the controller of active power filter mainly divided into two parts i.e. reference current generation and PWM current controller. The PWM current controller is principally used for providing gating pulse to the active power filter. In reference current generation scheme, reference current is generated by using the distorted wave form. Many control schemes are there for reference current generation, such as p-q theory[9][10], Dead Beat controller[19] ,Adaptive control[18][23], Neuro[22-26],Fuzzy[27][28],wavelet control[29-33] ,sliding mode control [29]-[33],delta-sigma modulation[14][15] vector control[13] , SFX control[18], repetitive control[17], etc. for improving the steady state and dynamic performance of APFs. By implementing this performance, response as well as the cost is efficient compare to the analog one.



Fig 1: General Block Diagram For shunt a Active filter

2.1. Fixed Frequency Control

The fixed frequency control [6] has been shown in figure 2 in which the error between the reference and actual current is fed through a PI controller which integrates the error between the feedback and reference current to generate a variable voltage value then, this value is fed into a triangle pulse-width modulator to produce gate signal. The output V control of the amplifier is compared with a fixed frequency (switching frequency fs) triangular waveform v_{tri} . A positive error ($i_A^*-i_A$) and hence a positive Control result in a larger inverter output voltage, thus bringing i_A (phase A current) to its reference value(i_A^*). Similar action takes place in other two phases.



Fig:2 Fixed Frequency Control

2.2. SFX Algorithm Based Adaptive Control

This method proposes a new current control method for active power filters (AF's). It requires only detecting the source current. It requires neither detecting AF output current nor extracting a harmonic component from the source current. Thus, the current control system can be greatly simplified compared to conventional load current detection AF's. It introduces an adaptive digital filter (ADF) with a synchronized filtered-x (SFX) algorithm [18]. An SFX-ADF based current controller exhibits a high gain only at fundamental and harmonic frequencies that the load current control system. The proposed method is suitable for filtering harmonic currents from one or more specified harmonic-producing loads. This method requires the following conditions.

- The harmonic-producing load has current-source
- Both harmonics and reactive power compensation is done by AF

The Source currents is given by the difference of load and controller current $I_s = I_L - I_C$. Consider a finite impulse digital filter with a desired response and error signal shown in Fig. 3. The input x and output y of ADF has the following relation

$$y(n) = \sum_{k=0}^{N-1} w_k(n) x(n-k)$$
(1)

$$\varepsilon(n) = d(n) - y(n) \tag{2}$$

The ADF operates so as to.minimize the mean-square error by renewing the filter weights at every sampling. If the LMS (least-mean-square) algorithm is used, the filter weights are renewed as follows:

$$w_k(n+1) = w_k(n) + 2\mu\varepsilon(n)x(n-k)$$
Where k=0, 1,...., N-1
(3)

A serious problem of this ADF is that the adaptive algorithm involves a lot of calculations that should be executed within a short sampling time period. Therefore, the order of ADF 'N' must be limited and as a result the upper frequency of the output is limited .To solve this problem this paper introduces a synchronized filtered-x (SFX) algorithm. The SFX algorithm [18] is suitable for controlling periodic signals, and has been used for controlling noises and Vibrations. Its advantage is that the amount of calculation of the adaptive algorithm can be greatly reduced; the proportional regulator with gain K_p is also used to improve the dynamic performance of the current control system. The SFX-ADF based controller automatically adjusts its transfer function to minimize the mean-square error ε^2



Fig:3 Current Control Scheme For SFX-ADP

2.3. Delta Modulation (DM)

The Delta Modulation method [14][15] is a variation of the traditional hysteresis current regulator. This method consists in applying a constant voltage in all the switching period. The aim of the control is to obtain the switching signals from the comparison between the a fixed tolerance band and current error (normally this band is close to 0). If the mismatch between the actual and reference current is positive, the inverter output voltage must be positive and if there is mismatch then, the inverter voltage output must be negative. During a regular interval T_{sw} synchronized with the switching frequency, the voltage is held constant. If the Delta Modulation [15] is used, the current generated at the (k+1)th sampling time instant.

2.4. Dead-Beat Current Control

In the conventional digital dead-beat control schemes, the regulator calculates the phase voltage to make the phase current reach its reference by the end of the following modulation period. In this Control, a modified Method Based on Dead-Beat controller is used (MBDB)[19][20]. The purpose of this method is to compute directly the time period when a switching device is turned on in order to make the phase current reaches its reference by the end of the following modulation period. Figure 4 shows the basic principle of this control strategy for a single-phase equivalent.



Fig 4: Dead Beat Controller

2.5. Soft computing Techniques:

Soft computing Technique is a technology to extract information from the process signal by using expert knowledge [21][36]. It either seeks to replace a human to perform a control task or it borrows ideas from how biological systems solve problems and applies it to control processes. The main areas in soft computing notably are fuzzy logic [27][28], neural network[22] -[26], Wavelet control[29]-[33], genetic algorithm (GA), rough sets, etc.[36]

2.5.1. Neuro Controller

The different described neural networks have been trained and simulated in many different ways with the goal of getting the parameter values that produce the optimal performance for each of the topologies [23]. Two different performance indices were chosen to evaluate the effectiveness of each studied network by both of the operation strategies the reached accuracy level (in terms of the error rate) and the required training time [26]. With respect to the accuracy level, at the measuring of the harmonic coefficients an error rate of 1% were estimated as minimum desired accuracy. With respect to the training time, network topologies and parameters were desired, that could be trained in so short times as possible. For this purpose it were compared the needed times to achieve similar accuracy levels [22]-[25]. The software tool employed has been the Neural Networks Toolbox of MATLAB. All types of available training algorithms were used and tested, and the most efficient was found to be the Levenberg-Marquardt modified Back propagation



Fig: 5: Structure of Nero Controller

2.5.2. Fuzzy Controller

Fuzzy control [27] involves four stages: fuzzification, knowledge base, inference mechanisms, and defuzzification. The knowledge bases are designed to provide a good dynamic response under uncertainty in process parameters and external disturbances. In recent years, fuzzy logic controllers [28] have generated a great deal of interest in certain applications. The advantages of fuzzy logic controllers are: robustness, no need to accurate mathematical model, can work with imprecise inputs, and can handle non-linearity. The compensation process is based on sensing line currents only, an approach different from Conventional methods, which require harmonics or reactive volt-ampere requirement of the load

Fuzzy logic serves to represent uncertain and imprecise knowledge of the system, whereas fuzzy control enables taking a decision even if we can't estimate inputs/outputs only from uncertain predicates. The synoptic scheme of Fig. 6 shows a fuzzy controller [27], which possesses two inputs and one output. The inputs are namely the error (*e*), which is the difference between the reference current (harmonic current) and the active filter current (injected current) ($e = i_{ref} - i_f$) and its derivative (d_e) while the output is the command(c_{de}).[56]



Fig: 6: Structure of Fuzzy Controller for APF

2.5.3. Wavelet Controller

A Wavelet Transform (WT) based Technique to extract fundamental frequency component from a Non sinusoidal and unbalanced load current in a three phase System. The fundamental frequency component is extracted using Multiresolution analysis (MRA) [29]. The remaining harmonics can be used by the active filter for compensation .The constructed controller based on multi resolution analysis has the advantage of better frequency bandwidth selection. The tuning parameters of the controllers are the gains to be applied to the time-frequency signals at different resolutions. This has the advantage of aiding the process of a proper choice of coefficients of tuning parameters. The extraction by FFT leads to inaccurate results if the signal is contaminated by noise. The main problem of the Fourier transform is the number of points in the observation window, which should be a multiple of the numbers of samples per period. When the fundamental's frequency varies around the 50Hz value, this corresponds to a Modification in the number of samples per period. As a result, the accuracy of the extraction is reduced.

The Wavelet Transform based technique can eliminate the above mentioned drawbacks up to certain extent. Wavelet analysis is a new development in the area of applied mathematics [31]. Fourier analysis is ideal for studying stationary data (data whose statistical properties are invariant over time) but is not well suited for studying data with transient events that cannot - be statistically predicted from the data's past. In many filtering applications we need filters with symmetrical coefficients to achieve linear phase. None of the orthogonal wavelet systems except Haar are having symmetrical coefficients. But Haar is too inadequate for many practical applications. Biorthogonal wavelet system can be designed to have this property.



Fig: 7: Structure of wavelet Controller for APF

3. Conclusion

Active power filters are the emerging devices, which can perform the job of harmonic elimination properly. Most of the proposed control strategies for power quality improvements have been reviewed with regard to performance and implementation. This work reveals that there has been a significant increase in interest of active power filters and its control methods. This could be attributed to the availability of suitable power-switching devices at affordable price as well as new generation of fast computing devices (microcontroller and DSP and FPGA) at low cost. It is hoped that this survey on control techniques for active power filters will be a useful reference to the users and manufacturers.

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