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# Iterative Learning Control for a Repetitive 2D Process

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# **Iterative Learning Control for a Repetitive 2D Process**

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**Abstract:** To control the systems which operate in a repetitive mode is known to be a difficult task. For example, systems like robotic manipulators are used for doing the repetitive assigned task. This can be overcome by the iterative learning control (ILC) method. In this article, the DC motor model is discussed for controlling the speed of the motor by the design of an ILC algorithm, and its performance is compared with the conventional PID algorithm. The ILC algorithm is implemented for the minimum and non-minimum phase systems. The resulting response, absolute and integral absolute errors are plotted. The Z-N tuning method is used to implement PID control. The performance of the DC motor has been evaluated by comparing the Integral Square Error (ISE), Integral Absolute Error (IAE), and Integral Time Absolute Error (ITAE) with the conventional PID controller.

Keywords: DC motor, Iterative Learning Control (ILC), Z-N tuning, Integral Absolute Error (IAE), Integral Square Error (ISE), Integral Time Absolute Error (ITAE).

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# 1. Introduction

The performance of a system that executes the same work many times can be improved by learning from the past execution (iteration). The improvement in performance can be achieved by connecting the control for subsequent iterations with error information. This technique is used to control the systems operating in a repetitive or trial-to-trial mode with the requirement that a reference trajectory  $y_d(t)$  is defined over a finite interval  $0 \le t \le \alpha$ , where  $\alpha$  denotes the trial length, is followed to high precision.

A typical control law is given in (1) the following form:

$$u_{k+1}(p)=u_k(p)+\Delta u_{k+1}(p), k\geq 0$$
 (1)

The general block diagram for an ILC controller is given in the Figure 1.

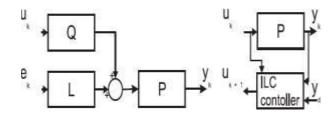


Figure 1. Block Diagram for an ILC controller

Two-dimensional process-based iterative learning control using noncausal finite-time interval data was explained by Błażej.C, Krzysztof.G, and Eric Rogers [1]. M. Rezaei and Kerman [2] have discussed PID parameter selection based on iterative learning control. Iterative learning control law design for an experimentally supported 2D process for error convergence and performance improvement was proposed by Lukasz. Hi and Krzysztof.G [3]. A review of repetitive control, run-to-run control, and ILC was proposed by Y.Wang, Furong.G, and Francis J. Doyle III [4]. Kevin L. Moore, Yang Q.C, and Hyo-Sung A [5] were explained about iterative learning control. Bristow, D.A. Tharayil, and M.Alleyne, A.G. [6] detailed ILC in a survey of iterative learning control paper. Owens D.H. [7],[8] presented optimization in iterative learning control. Jian-Xin Xu; Ying Tan[9] deals with both linear, nonlinear ILC. P. J. Schutyser [10] presented an approach to noncausal iterative learning Control. R.W. Longman [11] has discussed the ILC and repetitive control. Linear iterative learning control schemes were analyzed and explained elaborately by N. Amann, D. H. Owens, E. Rogers [12]. The paper is discussed in the following manner: Section 2 discusses the DC motor model. Section 3 explains the design of the ILC algorithm. Section 4 compares ILC and conventional algorithms. Section 5 includes the simulation results of the DC motor, and the performance criteria were evaluated. Finally, Section 6 concludes how the ILC is better than the conventional PID controller.

#### 2. Modelling of A DC Motor

An electromechanical DC motor system is considered in this work. From the electrical system with field and armature circuit, only the armature is considered for the analysis as the field is excited by a constant voltage. The rotating part of the motor and load connected to the shaft on the mechanical system side. The DC motor speed control system with armature is shown in Figure 2.

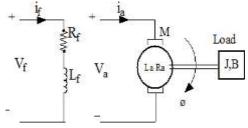


Figure 2. DC Motor Circuit

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Where,

Ra is the Armature (Electric) resistance ( $\Omega$ )

La is the Armature (Electric) inductance (H)

ia is Armature current (A)

Va is Armature voltage (V)

eb is the Back emf (V)

Tq is the torque developed by the motor (kg.cm)

ω is Angular displacement (rad/sec)

J is the Moment of inertia of motor (kg.m<sup>2</sup>)

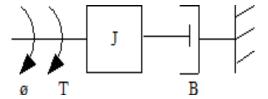


Figure 3. Mechanical Equivalent of the DC Motor

$$J_{\frac{d^2\theta}{dt^2}} + B_{\frac{d\theta}{dt}} = KI \tag{2}$$

$$Js^{2}\theta(s) + Bs\theta(s) = KI(s)$$
(3)

Where,

J is Motor's moment of inertia (kg.m<sup>2</sup>)

B is Motor and load's frictional coefficient (Nm.s)

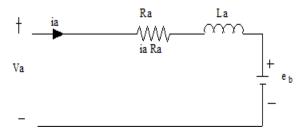


Figure 4. Electrical Equivalent of the DC Motor

$$eb = K\omega = K\frac{d\theta}{dt}$$
 (4)

eb is the back emf

Applying Kirchhoff's voltage law

$$L_{dt}^{di} + Ri = V - e_b \tag{5}$$

$$LsI(s) + RI(s) = V - Ks\theta(s)$$
(6)

$$I(s) = \frac{V(s) - K s\theta(s)}{R + L s}$$
(7)

Using the equations (2)-(7) obtained from the mechanical equivalent shown in Figure 3 and electrical equivalent shown in Figure 4, we can arrive at the transfer function of the DC motor.

$$G(s) = \frac{\omega(s)}{V(S)} = \frac{K}{[(R+S)(JS+B)+K^2]}$$
(8)

#### 3. Design of ILC Algorithm

#### 3.1 Design Guidelines

The closed-loop system transfer function is represented by P(s), and the controller is represented by P(s), and ILC is shown in Figure 5. The tracking error ek is an error at P(s) at the controller is given as input to the controller. It is found by comparing measured position P(s) with reference position

yd. All iterations are of same finite time length,  $t \in [to tend]$ . The repetitive part of the error signal is reduced by ILC during the iterations. From one iteration to another, the feedforward signal is calculated by tracking error, and by using the learning filter L, the error is filtered and included in the forward path.

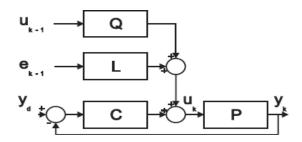


Figure 5. Block diagram of ILC Architecture

L, Q is ILC parameters to be chosen in accordance with the convergence criteria, where P is the process's transfer function, and SP is the sensitivity function of the same.

The control law (9) linear time-invariant (LTI) system  $y_k = P^*u_k$  is as

$$u_{k+1} = Q^*u_k + L^*(y_d - y_k) \tag{9}$$

A sufficient condition for convergence of the above equation for all yd and all uo is given in (10)-(15):

$$|Q - SP*L| < 1 \tag{10}$$

Proof

$$e_k = y_d - y_k$$
, and  $y_k = P^*u_k$  (11)

$$u_{k+1} = Q^*u_k + L^*e_k$$
 (12)

$$e_{k+1} = y_d - P^*Q^*u_k - P^*L^*e_k$$
 (13)

$$e_{k+1} = y_d - Q^*y_k - P^*L^*e_k$$
 (14)

$$e_{k+1} = (1 - Q)y_d + (Q - P*L)e_k$$
 (15)

The error will decrease when Q and L satisfy the criteria equation of convergence. The fixed point error will be reached after one trial when (Q - SP\*L) = 0. The error will increase every trial, and finally, the output won't be bounded when Q - SP\*L > 1 then. The error will be constant as in (16) after a number of trials because it converges to a fixed point.

$$Lime_{k+1} = lim e_k = e^*$$
(16)

 $k \rightarrow \infty$   $k \rightarrow \infty$ 

Then the asymptotic error  $e^*$  is given by equation (17)

$$e^* = \frac{(1-Q)}{1-Q+P*L} y_d \tag{17}$$

And the asymptotic input is in (18) - (20)

$$u^* = \frac{L}{1 - Q + P * L} y_d \tag{18}$$

$$\mathbf{u}^* = \mathbf{Q}\mathbf{u}^* + \mathbf{L}\mathbf{e}^* \tag{19}$$

$$u^* = \frac{1}{1 - 0} e^* \tag{20}$$

#### 3.2 Learning filter Design (Design of L)

The selection of Q and L is necessary for the design of the ILC controller for an LTI process for achieving good performance. From the asymptotic error equation, it can be observed that Q must be chosen close to unity to get a minimum error. From the convergence criteria, the learning filter should

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obey the convergence rule and can be obtained as (SP)-1 for fast convergence.

Using the closed-loop transfer from the feedforward to the error signal, i.e., the process sensitivity SP = P/(1+P\*C). The propagation of the error from iteration to iteration can be written as  $e_k+1 = (Q-SP*L)e_k$ .

# 3.3 Design of Q

Q should be designed in such a way that L becomes a proper transfer function when multiplied with Q.

Example given in (21)-(23)

For first order Q,

$$Q = 1/(\varepsilon s + 1) \tag{21}$$

Second order Q,

$$Q = 1/(\varepsilon s + 1)^2 \tag{22}$$

Third order Q,

$$Q = 1/(\varepsilon s + 1)^3 \tag{23}$$

The value of  $\epsilon$  should be appropriately chosen. As  $\epsilon$  value is greater, the settling time for the response is greater. For small values of  $\epsilon$ , the settling time is minimum.

# 4. Comparison of ILC And Conventional Algorithm

The ILC algorithm has been implemented for a minimum phase system, and various values of  $\varepsilon$  in Q design; the response plots are shown in Figures 6, 7, and 8.

# 4.1 Simulation Results for Example 1

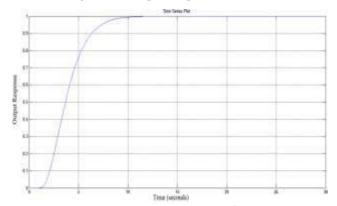
$$G(s) = \frac{1}{s^3 + 3s^2 + 3s + 1}$$

$$\frac{1}{1 + 3s^2 + 3s + 1}$$

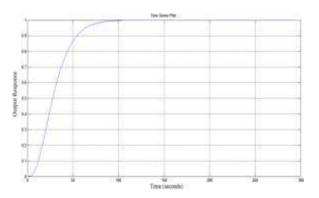
$$\frac{1}{1 + 3s^2 + 3s + 1}$$

$$\frac{1}{1 + 3s^2 + 3s + 1}$$

**Figure 6.** Output response when  $\varepsilon = 0.1$ 



**Figure 7.** Output response when  $\varepsilon = 1$ 



**Figure 8.** Output response when  $\varepsilon = 1$ 

The PID controller is designed for DC motor velocity control. The mathematical representation of the

PID controller is given in (24)
$$C(S) = K_p + \frac{k_i}{s} + K_d S$$
(24)

The Z-N tuning method can be used to evaluate Kp, Ki, and Kd.

The Table 1 and 2 show the performance indices ISE, IAE, ITAE for the systems designed under the ILC and Conventional algorithms. The simulations of the algorithms are done for three types of systems, namely: Minimum Phase System, Non-minimum phase system, and Delayed system.

Table 1. Performance Indices of Systems Design Under **ILC** Algorithm

ILC Algorithm			
Systems	Performa	Performance Indices	
Minimum Phase System	ISE	:0.2062	
1	IAE	: 0.3	
$\overline{S^3 + 3S^2 + 3S + 1}$	ITAE	: 0.36	
Non-Minimum Phase	ISE	: 0.5	
System	IAE	: 1.0	
$\frac{-S+1}{S^2+S+1}$	ITAE	: 2	
Delayed System	ISE	: 1.25	
1	IAE	: 2	
$\overline{S^2 + 3S + 1}$	ITAE	: 5	

Table 2. Performance Indices of Systems Design Under **Conventional Algorithm** 

Systems	Performan Indices	ce
Minimum Phase System $\frac{1}{S^3 + 3S^2 + 3S + 1}$	ISE IAE ITAE	:0.81 : 0.725 :0 .712
Non-Minimum Phase  System $\frac{-S+1}{S^2+S+1}$ Delayed System $\frac{1}{S^2+3S+1}$	ISE IAE ITAE ISE IAE ITAE	: 2.076 : 2.22 : 5.602 :0.541 :0.451 :2.41

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# 4.2 Uncertainty Handling

For the minimum phase system discussed above, the ILC algorithm is executed when the system has 10% uncertainty. The output response of a step signal in the presence of 10% uncertainty is shown in Figure 9.

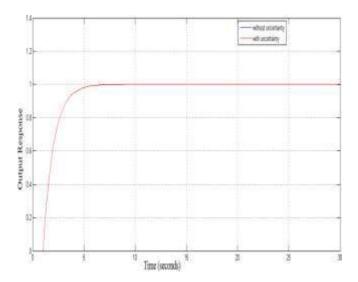
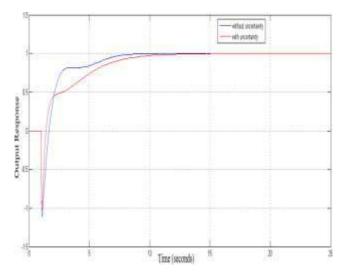


Figure 9. Output Response of ILC under 10% uncertainties

For the same minimum phase system, when handled by a conventional algorithm, the output response of a step signal in the presence of 10% uncertainty is shown in Figure 10.

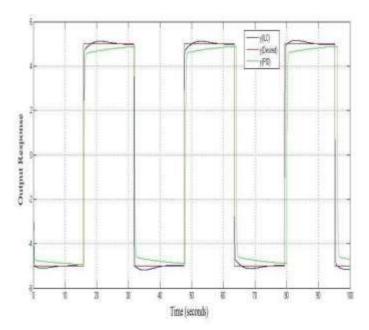


**Figure 10.** Output Response of conventional algorithm under 10% uncertainties

# 5. Results and Discussion

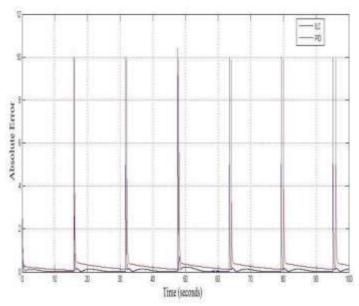
The proposed ILC controller is designed for the DC motor through simulation. The same is done using the conventional method. This is done for comparison purposes.

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**Figure 11.** Output responses of ILC & Conventional Algorithms

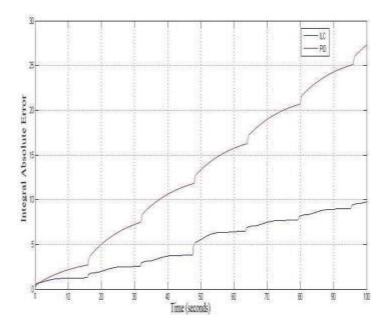
Figure 11 shows the output response plot of both ILC and conventional algorithms. From the figure, it is clear that the rise time of an ILC algorithm is less when compared to a conventional algorithm. And there is an offset present in the conventional algorithm, and it is eliminated by the ILC algorithm. The Absolute Error versus Time plot is shown in Figure 12, and it reveals that the area under the graph is greater for the conventional algorithm than that of the ILC algorithm.



**Figure 12.** Plot of absolute error using ILC & PID for the DC Motor

The Integral Absolute Error plot is shown in the Figure 13 from that it is observe that the error value for conventional algorithm keeps increasing by large number in comparison with that of the ILC algorithm.

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**Figure 13.** Performance measure of the DC Motor using ILC & PID Algorithms

#### 6. Conclusion

The application-based implementation, as well as the test case systems of the ILC algorithm, are the evidence to validate the superiority of the ILC algorithm over the conventional algorithm. The ILC algorithm offers better speed control than the conventional control algorithm. The rise time is reduced, and the settling time is also lower, thus offering better efficiency. Thus the objective is validated, and hence we can conclude that the ILC algorithm offers better performance.

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