Received: 4 May 2017 DOI: 10.1002/mop.30865

Modeling of field dependent Maxwell-Wagner interfacial capacitance for bilayer metalinsulator-metal capacitors

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Abstract

In this letter, we have presented the modeling of field dependent Maxwell-Wagner interfacial capacitance for bilayer metal-insulator-metal (MIM) capacitors. The model was verified with measured capacitance-voltage characteristics of fabricated bilayer Al_2O_3/TiO_2 MIM capacitors. The model reveals the origin of voltage linearity of MIM capacitors at low frequencies (<10 kHz). The proposed model for bilayer/multilayer MIM capacitors is very useful tool to design circuits for mixed signal, analog and digital circuits with low variation of capacitance for change in voltage.

KEYWORDS

anodization, frequency dependant capacitance, high-k, MIM capacitor, voltage linearity

1 | INTRODUCTION

Metal-insulator-metal (MIM) capacitors are popular and attractive passive elements for analog and mixed signal (AMS) applications. MIM structures have weak dependence of capacitance with applied voltage since there is no oxide/ oxide or oxide/semiconductor interface. This dependence is often referred as 'voltage linearity' or 'voltage coefficient of capacitance (VCC)'. Some of the sensitive circuits, such as A/D converter ICs, expect a very low VCC of less than 100 ppm V⁻² with high capacitance density and low leakage current density. Many dielectric materials were studied in the past decade to analyse the VCC, however, no single dielectric layer MIM capacitors could show such a low VCC till date. In this regard, various high-*k* dielectric stack MIM

capacitors were proposed by many authors in recent days.^{1–4} Although stacked MIM capacitors show low VCC at high frequencies, the frequency dependent voltage linearity reveals that VCC is very high about $>10^4$ ppm V⁻² at low frequencies.⁴ Interfacial polarization or electrode polarization is assumed as origin of such increase in VCC as a result of field dependent permittivity.

Researchers have developed analytical or empirical models for voltage linearity using various micro and macroscopic polarization mechanisms in dielectrics, such as orientation polarization,⁴ electrode polarization,⁵ electrostriction,⁶ and ionic polarization.⁷ Recently, our group has demonstrated the modeling of voltage linearity of high-k dielectric MIM capacitor using microscopic and macroscopic models of ionic and orientation polarization.8 It is observed that required thickness increases with dielectric constant. This model predicts that the thickness of dielectric layer should be \sim 110 nm for higher dielectric constant (>30) to achieve VCC of 100 ppm V^{-2} . Although, this model shows a good agreement with experimental results at 100 MHz, it does not fit for VCCs at low frequencies. It is observed that the value of VCC is higher at low frequencies. This could be due to migration and accumulation of charges at the interface of dielectrics which is dominant at low frequencies. Such accumulated charges polarize for the applied field, such mechanism is referred as Maxwell-Wagner (MW) polarization.9

MW polarization is observed in many ferroelectric mixtures or heterostructures.¹⁰⁻¹⁴ The accumulated charges at the interface of heterostrucutres for the applied field lead to the enhancement of effective permittivity. A giant dielectric constant of 1000 was achieved with ALD TiO₂/Al₂O₃ nanolaminates by Li et al.¹⁵ Because of MW polarization, the each TiO₂/Al₂O₃ interface accumulation yields a high dielectric constant of 750 times greater than that of Al₂O₃. Recently, MW effect was found in MOS device by Jinesh et al.¹⁶ It is worth to note that the MW charge accumulation is observed in forward bias only, i.e., the charge injection from high conductive semiconductor to low conductive highk material. The approach of imaginary permittivity ε'' to unity in forward bias at low frequencies indicates the presence of MW polarization.¹⁶ However, models developed in these reports have ignored the dependence of applied potential across the dielectric stack.

Like single layer dielectrics MIM capacitors, modeling of capacitance-voltage characteristics of bilayer or multilayer MIM capacitors are not reported yet as per our knowledge. In this letter, we have developed a model for voltage dependence of bilayer dielectrics MIM capacitor using MW polarization. This model considers the carrier tunnelling probability of dielectric stack and MW relaxation time of accumulated interfacial charges. A good agreement was found between the model and the measured capacitance-



FIGURE 1 SEM cross section image of anodized bilayer region before top electrode deposition, (A) sample A1: AV = 25 V, (B) sample A2 AV = 30 V

voltage characteristics of TiO_2/Al_2O_3 MIM capacitors. It is observed that the MW polarization is significant in low frequencies of <10 kHz.

2 | FABRICATION AND CHARACTERIZATION

A 100 nm SiO₂ isolation layer was grown on Si substrate and thoroughly cleaned by deionized water. Over that, a bilayer of 15 nm Ti on 100 nm Al is deposited using electron beam evaporator with tungsten filament at a pressure of 8×10^{-5} mBar. This Ti/Al film was anodized potentiostatically using nonaqueous solution of ammonium pentaborate dissolved in ethylene glycol (20 g l^{-1}) by the same size of platinum cathode. Oxidation was done for various anodization voltages of 15, 20, 25, and 30 V till the anodization current density reduces to 1 μ A cm⁻². Only three-quarters of sample area was dipped in the electrolyte to avoid etching for bottom electrode. This forms a barrier type anodic TiO₂ and bilayer of TiO₂/Al₂O₃ at lower and higher anodization voltages respectively. After cleaning thoroughly by deionized water, a 50 nm thick Al top electrode was deposited on the samples using thermal evaporation with the shadow mask area of 0.61 mm². Bilayer TiO₂/Al₂O₃ MIM capacitors anodized at 25 and 30 V are named samples A1 and A2, respectively.

Scanning electron microscope (SEM) cross-section views of these sample are shown in Figure 1. The anodized secondary layer of Al_2O_3 is visible between Anodized TiO₂ and Al. The penetration depth was found about 2–3 nm. Figure 2 shows the depth profile of both samples using secondary ion mass spectrometry (SIMS) in positive mode with 1 kVCs. It shows ion distribution of Ti, Al, O, Si, Ti-O, and Al-O. At low anodization voltages (15 V and 20 V), Al bottom electrode is also slightly anodized which was reported in Ref. [17]. This is due to outward migration of Al ion which forms a thin interface layer of AlTiO. At higher anodization voltages (25 V and 30 V), the inward migration of oxygen ion increases which forms a thin layer of Al₂O₃. It is observed that outward migration of Al into TiO₂ is increased which increases the thickness of AlTiO composite layer. Xray diffraction patterns of the samples are shown in Figure 3. It is observed that the crystalline phases of TiO₂ anatase and rutile are present at low anodization voltages (<20 V). At higher anodization voltages, the crystalline γ -Al₂O₃ emerges at $2\theta = 65.5^{\circ}$. Crystallization of anodic TiO₂ has been addressed by many authors and summarized in our earlier work.¹⁷ Readers are recommended to read the nucleation/ crystallization of anodic bilayer oxides reported in Refs. [18] and [19].

The low frequency capacitance and leakage current density were measured using semiconductor parameter analyzer (HP4155C). The measured capacitance-voltage (CV) characteristics of samples A1 and A2 at 1, 5, and 10 kHz are shown in Figure 4. The CV characteristics are asymmetric and



FIGURE 2 SIMS depth prole: (A) sample A1: AV = 25 V, (B) sample A2 AV = 30 V. [Color figure can be viewed at wileyonlinelibrary. com]



FIGURE 3 X-ray diraction spectra of anodized samples at various anodization voltages (A) 15 V, (B) 20 V, (C) 25 V, (D) 30 V (A: anatase, R: rutile). [Color figure can be viewed at wileyonlinelibrary.com]

frequency dependent. This is due to the accumulation of charges for reverse bias which enhances the capacitance. Also, the maximum capacitance at reverse bias decreases with increment in frequency. Figure 5 shows the measured leakage current density as a function of applied voltage at room temperature. It has been observed that both samples show a high degree of asymmetry between forward and reverse biases. This is due to the formation of AlTiO interfacial layer, TiO₂/ Al_2O_3 stack and interfacial traps/charges. A detailed discussion of leakage characteristics and conduction mechanism of these bilayer MIM capacitors are reported in Ref. [20].

3 | VOLTAGE DEPENDENT MW CAPACITANCE MODEL

A bilayer MIM structure is considered as shown in Figure 6A. The layers consist of two dielectric materials with distinct relative dielectric constant of ε_{r1} and ε_{r2} with thickness of d_1 and d_2 respectively. Each layer's conductivity and relaxation time are represented as σ_n and τ_n , respectively, for



FIGURE 5 Measured leakage characteristics of both samples at room temperature

n = 1, 2. Figure 6B shows the equivalent RC network of bilayer MIM structure. Here $R_1 \& C_1$ and $R_2 \& C_2$ are individual resistance and capacitance of layer-1 and layer-2 respectively. C_{MW} is interfacial capacitance, also called MW capacitance, which is significant at low frequencies. The total capacitance can be expressed as:

$$C_{\rm tot}(V_{\rm B}) = \left(\frac{1}{C_1} + \frac{1}{C_2}\right)^{-1} + C_{\rm MW}(V_{\rm B}) \tag{1}$$

where MW capacitance can be calculated by

$$C_{\rm MW} = Aq^2 N_{\rm MW} \tag{2}$$

Here q is charge of an electron and A is top electrode area of capacitor. $N_{\rm MW}$ is the accumulated charge density at the interface for the applied bias voltage $V_{\rm B}$. According to MW theory of double layer,^{21,22} the accumulated charge density at the interface as a function of applied potential and time is expressed as:

$$N_{\rm MW} = \varepsilon_0 \left[\frac{(\varepsilon_{r1} \sigma_2 - \varepsilon_{r2} \sigma_1)}{(\sigma_1 d_2 + \sigma_2 d_1)} \right] \cdot V_{\rm stack} \cdot \left[1 - e^{\left(-\frac{t}{\tau_{\rm MW}} \right)} \right], \quad (3)$$

Here V_{stack} is voltage across the bilayer dielectric stack for the applied bias voltage V_{B} , t is time period of



FIGURE 4 Measured capacitance voltage characteristics for low frequencies at room temperature: (A) sample A1 and (B) sample A2

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FIGURE 6 Schematic of bilayer configuration: (A) layer specification, (B) equivalent circuit at low frequencies. [Color figure can be viewed at wileyonlinelibrary.com]

TABLE 1 Fitting parameters of Maxwell-Wagner capacitance model

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Sample name	<i>d</i> ₁ (nm)	<i>d</i> ₂ (nm)	€ _{r1}	E _{r2}	σ_1 (pS cm ⁻¹)	σ_2 (pS cm ⁻¹)	φ ₁ (eV)	φ ₂ (eV)	V _{bi} (V)
A1	15	7	90	9	15	2×10^{-3}	3.3	2.3	1
A2	15	10	90	9	15	2×10^{-3}	3.3	2.3	1.2

measurement or time period of the one full cycle of AC signal, and τ_{MW} is relaxation time of double layer. The accumulated interface charges and native traps built up potential which opposes the applied field and reduces flow of charges, so the potential across stack can be written as $V_{\text{stack}} = |V_B - V_{\text{bi}}|$. Here V_{bi} is built in potential at interface. MW time constant can be expressed as

$$\tau_{\rm MW} = \frac{d_1 \varepsilon_2 + d_2 \varepsilon_1}{\sigma_1 d_2 + \sigma_2 d_1} \tag{4}$$

If the measurement time/period of the cycle *t* is very greater than τ_{MW} , then the third term of eq. 3 can be eliminated. Therefore, we shall rewrite eq. 3 as:

$$N_{\rm MW} = \frac{\sigma_1 \sigma_2 \left(\frac{\varepsilon_1}{\sigma_1} - \frac{\varepsilon_2}{\sigma_2}\right)}{d_1 d_2 \left(\frac{\sigma_1}{d_1} + \frac{\sigma_2}{d_2}\right)} \cdot |V_{\rm B} - V_{\rm bi}|$$
(5)

We know $\tau_1 = \frac{\varepsilon_1}{\sigma_1}$ and $\tau_1 = \frac{\varepsilon_1}{\sigma_1}$. Conductance $G_1 = \frac{1}{R_1} = \frac{\sigma_1}{d_1}$ and $G_2 = \frac{1}{R_{21}} = \frac{\sigma_2}{d_2}$. By substituting these expression in eq. 3



FIGURE 7 MW capacitance voltage model (without tunneling probability) and measured C-V fitting compatibility for samples (A) AT-3 and (B) AT-4



FIGURE 8 MW capacitance voltage model (with tunneling probability) and measured C-V fitting compatibility for samples: (A) AT-3 and (B) AT-4

$$N_{\rm MW} = \left(\frac{G_1 G_2}{G_1 + G_2}\right) . (\tau_1 - \tau_2) . |V_{\rm B} - V_{\rm bi}| = G. (\tau_1 - \tau_2) . |V_{\rm B} - V_{\rm bi}|$$
(6)

Since, two conductance are in series, $G = \frac{G_1G_2}{G_1+G_2}$. On the other hand, imaginary part of dielectric permittivity is expressed as:¹²

$$\varepsilon'' = \frac{1 + \omega^2 [(\tau_{\rm MW}, \tau_1) + (\tau_{\rm MW}, \tau_2) - (\tau_1, \tau_2)]}{\omega C_0 (R_1 + R_2) (1 + \omega^2 \tau_{\rm MW}^2)}$$
(7)

and $G = \omega C_0 \varepsilon''$. C_0 is the capacitance at no applied voltage. Thus, the eq. 5 can be written as:

$$N_{\rm MW} = \frac{1 + \omega^2 [(\tau_{\rm MW}, \tau_1) + (\tau_{\rm MW}, \tau_2) - (\tau_1, \tau_2)]}{\omega C_0 (R_1 + R_2) (1 + \omega^2 \tau_{\rm MW}^2)} . (\tau_1 - \tau_2) . |V_{\rm B} - V_{\rm bi}|$$
(8)

The above equation describes the quantity of accumulated charges according to the MW theory. The parameters are assumed and calculated to validate this model as shown in Table 1. The model is accounted only for reverse and forward bias that considered as voltage independent. The compatibility of proposed model with measured capacitance for the applied voltages at various frequencies is shown in Figure 7A and B for samples A1 and A2, respectively. It is clear that the model is not fitting with measured C-V characteristics. This is due the linear relation of applied voltage with accumulated charge density as shown in eq. 8. This also reveals that charge migration between dielectric materials is ignored.

To incorporate realistic situation, tunnelling probability has been added with existing model based on Poole-Frenkle emission model.²³ Therefore, eq. 8 can be rewritten as:

$$N_{\rm MW} = \frac{1 + \omega^2 [(\tau_{\rm MW}, \tau_1) + (\tau_{\rm MW}, \tau_2) - (\tau_1, \tau_2)]}{\omega C_0 (R_1 + R_2) (1 + \omega^2 \tau_{\rm MW}^2)}$$

$$.(\tau_1 - \tau_2).|V_{\rm B} - V_{\rm bi}| \left\{ 1 - \exp\left(\frac{(qV_{\rm stack} - \phi_1 + \phi_2)}{kT}\right) \right\}$$
(9)

In this model, φ_1 and φ_2 are barrier potentials of dielectric layers. Table 1 shows the value of these potentials. Fitting compatibility of this proposed model with measured data is shown in Figure 8A and B. A close fit of model with tunneling probability is observed. Unlike high frequencies, the low frequency capacitance shows a striking asymmetry between forward and reverse biases. This similar asymmetric property was also observed in leakage characteristics of bilayer capacitors. This indicates the positive traps/charges are not only sensitive to temperature, but also with the applied electric field direction and frequency of input AC signal. The polarization and accumulation of interface charges between two distinct dielectric materials are referred as MW polarization.

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MW effect of permittivity enhancement in bilayer MIM capacitors leads to increase in VCC at low frequencies. It is observed that the ratio of permittivity of both dielectric material ($R_{di} = \frac{\varepsilon_{r1}}{\varepsilon_{r2}}$) largely determines VCC which is responsible for MW capacitance. For instance, TiO₂/Al₂O₃ has $R_{di} = 90/9 = 10$ which shows a capacitance enhancement of twice compared to series capacitance of bilayer in our experiments. At the same time, sandwich of TiO₂ and Al₂O₃ multilayer stack shows a giant dielectric constant of >500 times of single layer MIM structure.¹⁵ Therefore, the dependence of capacitance with voltage can be reduced by choosing the materials with less R_{di} , such as ZrO₂/HfO₂ (29/25 = 1.16) and HfO₂/Al₂O₃ (29/9 = 3.22).

4 | **CONCLUSIONS**

This article reveals the origin of low frequency VCC of bilayer/multilayer MIM capacitors. Field dependent capacitance of bilayer MIM capacitors is deduced from Maxwell approach on accumulation of charges at dielectric interface. The voltage dependence of dielectric enhancement can be modeled with Wagner equation on space charge polarization. The model shows good agreement with experiment and ²⁹⁷⁰ WILEY-

confirms that the MW polarization occurs at low frequencies and largely depends on field direction. The charge built-up due to tunneling and accumulation has added more accuracy compared to ideal case. The model helps in selection of materials and design of low VCC MIM capacitors for highly sensitive applications such as medical devices and signal processing units.

ACKNOWLEDGMENTS

The authors would like to acknowledge financial support by Indian Nanoelectronics User's Program (INUP), Government of India, through Center for Excellence in Nanoelectronics, IIT-Bombay. Authors also would like to thank Prof. A. Q. Contractor and Dr. K. Nageswari of IIT-Bombay for their support.

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How to cite this article: Kannadassan D, Mallick PS. Modeling of field dependent Maxwell-Wagner interfacial capacitance for bilayer metal-insulator-metal capacitors. *Microw Opt Technol Lett.* 2017;59:2965– 2970. https://doi.org/10.1002/mop.30865

Received: 4 May 2017

DOI: 10.1002/mop.30864

Adjustable compact dual-band microstrip bandpass filter using T-shaped resonators

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Funding information

This work was supported by Semnan University.

Abstract

In this letter, a compact dual-band bandpass filter (BPF) is designed, analyzed, and fabricated. A loop resonator