

Hydrogen Silsesquioxane-Based Nanofluidics

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Nanofluidics show great promise for the control of small volumes and single molecules, especially for biological and energy applications. To build up more and more complex nanofluidics systems, a versatile and reproducible fabrication technique with nanometer precision alignment is desirable. In this article, two e-beam lithography methods to fabricate nanofluidic channels based on hydrogen silsesquioxane, a high-resolution negative-tone inorganic resist, are presented. The robustness and versatility of the fabrication processes are demonstrated on silicon, glass, and flexible substrates. The high precision ability is illustrated with nanometric alignment of nanofluidic channels on gold nanoparticles and nanotransistor sensors, as well as for 3D nanofluidics prototyping. Furthermore, an unexpected extremely slow water evaporation rate (≈ 1 week for 300 μm long nanochannels) is noticed. This feature enables a simple and reliable manipulation of nanofluidic chips for various studies.

optical, e-beam, nanoimprint lithography with sacrificial layers or etching.^[1,6–10] Another alternative is the use of nanoporous materials.^[11,12] Although these different methods are relatively simple, many of them are not compatible with a low-temperature process (e.g., for plastic substrates or organic devices), or do not offer good alignment ability on patterned substrates for rapid prototyping of complex nanofluidic systems (e.g., for hybrid nanofluidics/nanosensor devices or 3D nanochannels fabrication).

Si-based inorganic–organic polymers with a general structure of $[\text{R}_x\text{SiO}_y]_n$ (R is a hydrocarbon group) have the advantage of being processed at low temperature. However, they cannot be aligned at nanometer scale, and they have a small Young's modulus which prevents their use in nanoflu-

1. Introduction

Thanks to its unique features at the nanoscale, nanofluidics, the study and application of fluid flow in nanochannels/nanopores with at least one characteristic size smaller than 100 nm, has enabled the occurrence of many interesting transport phenomena and has shown great potential in both bio- and energy-related fields.^[1–5] The unprecedented growth of this research field is related to the rapid development of micro/nanofabrication techniques. Several methods have been developed thus far to afford the fabrication of nanochannels such as

idics. Hard polydimethylsiloxane (PDMS) (a PDMS engineered to have a higher mechanical modulus of ≈ 8 MPa) has been successfully used for nanofluidics,^[13,14] but its Young's modulus remains small. Polysilsesquioxane (PSQ) has a much higher Young's modulus (800 MPa).^[15] It has been successfully used as a simple sealing method,^[16] but it cannot be directly prototyped. Finally, hydrogen silsesquioxane (HSQ: Fox 16, Dow Corning) can be processed at low temperature, with the high precision alignment provided by e-beam lithography, and provides an additional advantage of planarity. It has often been used in nanoelectronics as a mask before silicon etching thanks to its high resolution (< 10 nm). It can be almost densified to SiO_2 after plasma or electron-beam exposition,^[17–19] and has also been used for 3D prototyping of photonic crystals,^[20] or for the fabrication of dielectric nanoantennas.^[21] However, except its use as a high-resolution mold^[22] or as channels arising from the collapse of thin HSQ walls,^[23] the unique advantages provided by this material have not been exploited to demonstrate a potential use in nanofluidics.

Here, we show that HSQ can be used as a structuring material for nanofluidics applications. We take advantage of the direct HSQ prototyping at nanometer scale and its high planarity to demonstrate two simple and versatile ways of fabricating nanofluidic channels. In particular, we show the possibility to fabricate 3D stacked layers, and to align nanochannels on nanostructured surfaces such as gold nanoparticles or nanotransistor biosensors. Limitations and related solutions specific to the use of HSQ for nanofluidics applications are presented as a guideline for practical use of HSQ in nanofluidics. We also evidence an extremely slow evaporation rate of water

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inside the channels covered by HSQ, an unexpected feature that significantly simplifies microscope studies of chips composed of nanofluidics channels.

2. Nanofluidic Channels Fabricated by Direct HSQ Prototyping (First Approach)

The first approach is based on direct prototyping of HSQ, being used as a negative tone electron beam resist. Technological steps for the proposed approach are summarized in **Figure 1a**. The starting substrate is an n-type bulk silicon wafer covered with native oxide. If necessary, alignment markers are made using standard photolithography. After substrate dehydration (at 180 °C for 10 min), HSQ is spin-coated to get an 850 nm thick layer. Then, the HSQ is exposed to e-beam and developed in tetramethyl ammonium hydroxide (TMAH)-25% solution for 90 s. It is then baked on a hot plate for 30 min at 110 °C followed by 30 min annealing at 180 °C to get robust hydrophilic and transparent nanofluidic channel walls. Using an aligned and patterned PDMS layer,^[24] nanofluidic channels are sealed and the connection to microfluidic channels is established. The distance between walls on access leads has to be small enough (below 3 μm) and sharp enough to avoid bonding of PDMS to the bottom of the channels. On the other hand, at the either ends of the HSQ walls, a smooth slope is required for proper PDMS/HSQ sealing in order to avoid undesired leakage. This 3D prototyping can be achieved by using different e-beam doses within a single writing step (less exposed areas appear

thinner after HSQ resist development (see, Figures S1 and S2, Supporting Information for more details)). **Figure 1b** shows a scanning electron microscope (SEM) image of the fabricated HSQ structure composed of parallel large channels with constrictions (nanofluidic channels) and also the smooth slope at the extreme ends of the array. The e-beam writing time was ≈1 min and the average roughness on the top of HSQ surface is 0.7 nm. A smooth slope of 10° on HSQ has been obtained from ten 90 nm thick, 500 nm wide stairs obtained from ten different exposure doses. The average nanofluidic channel width, obtained from 37 channels, is 97 nm ± 3.7 nm (**Figure 1c**) which highlights the reliability of the proposed technology. Nevertheless, we observe that 15% of the channels are out of this statistics that we attribute to the high aspect ratio (HSQ thickness vs channel width) required for nanofluidics application. All channels remain within an error of 25 nm above the average width. There is a tradeoff between the HSQ thickness and the channel width. For example, Grigorescu et al.^[18] reported a 10 nm gap for a 10 nm thick HSQ (aspect ratio of 1). Such a small layer thickness is well-suited when HSQ is being used as a mask for nanofabrication purposes. It is however not suitable to define walls of nanofluidics channels, mainly because PDMS can partly bind to the bottom of the channel in the 2 or 3 μm access leads. This is illustrated in, **Figure S3** (Supporting Information) for a 300 nm thick HSQ layer. We found that 400 nm can be considered as a minimum thickness and 850 nm a good tradeoff for high resolution, high reliability (no PDMS bonding in the channel), and reduced pressure drop (3 μm wide access leads can be used at this HSQ layer thickness). We also noticed

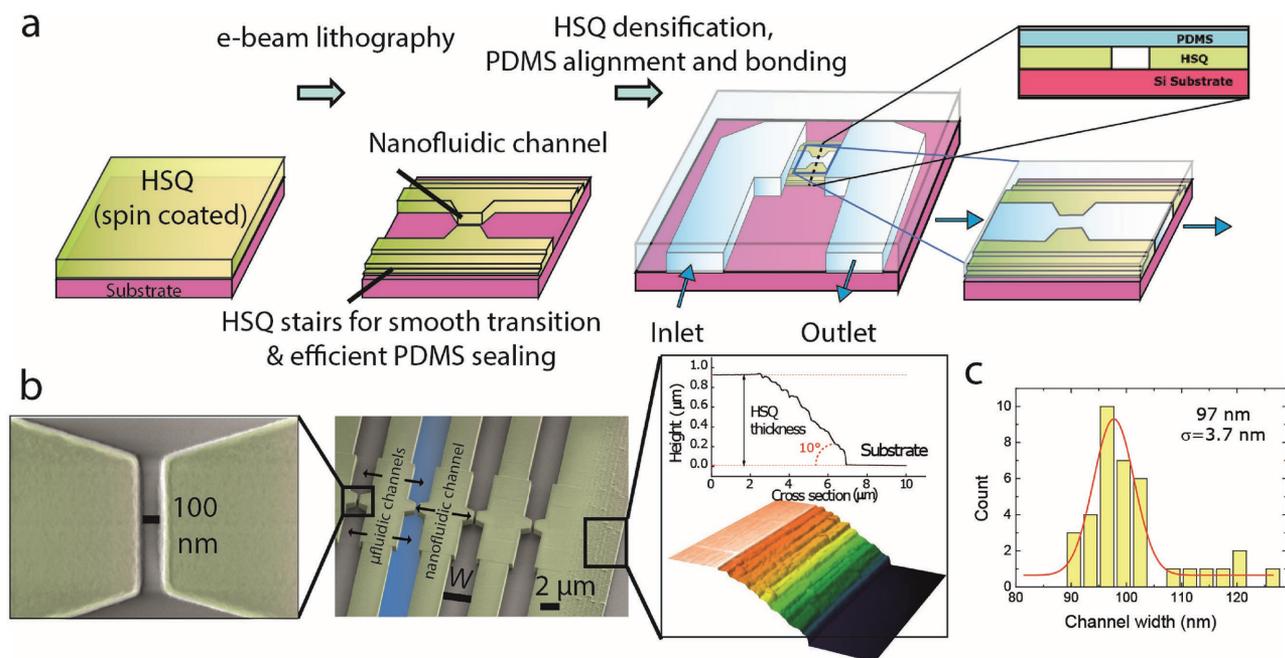


Figure 1. HSQ-based nanofluidics with direct HSQ patterning and PDMS bonding. a) Schematic view of the HSQ-based fabrication process. 850 nm thick HSQ is first spin-coated. HSQ walls and stairs are written within a unique and short e-beam lithography step (typically 1 min/chip). HSQ is baked on a hot plate for 30 min at 110 °C, followed by 30 min at 180 °C and the PDMS microfluidic channel is then aligned and bonded. b) Scanning electron microscope (SEM) top views of the HSQ nanochannels and atomic force microscope topography image of HSQ stairs with a cross section. Channel width W is 3 μm at inlets and outlets and below 100 nm at the nanoconstriction that defines the nanofluidic channel. c) Histogram of nanofluidic channel width measured by SEM for 37 channels. Average width is 97 nm with a standard deviation $\sigma = 3.7$ nm.

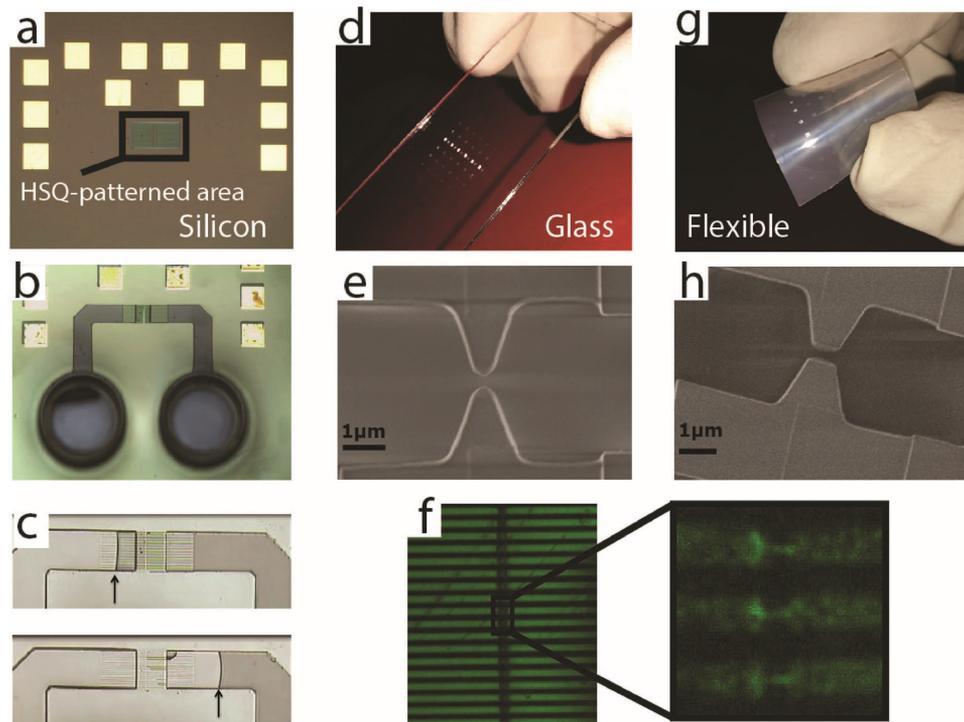


Figure 2. Versatility of HSQ-based nanofluidics. Silicon substrate: a) Optical image showing the HSQ-patterned area. b) Microfluidic channels aligned on the HSQ patterned area to form a microfluidic/nanofluidic transition. c) Demonstration of flow of DI water in the nanofluidic channels (water progress and the direction of the flow are indicated by an arrow). Transparent color corresponds to filled channels. Applied pressure was 3 bars. HSQ-based nanofluidics on glass substrate: d) Picture of the glass slide with HSQ-patterned areas. e) SEM image of the HSQ nanoconstriction. f) Confocal microscope image of the nanofluidic channels filled with Alexa-marked 25 ss-DNA. HSQ-based nanofluidics on flexible substrate: g) Picture of the flexible substrate with HSQ-patterned areas. h) SEM image of the HSQ nanoconstriction.

that there are no major differences in channel width resolution between 400 and 850 nm thick HSQ channels. Our best result with an 850 nm thick HSQ layer is 74 nm (aspect ratio of 12, see, Figure S4, Supporting Information). If a smaller gap is required for some applications (at high ionic strength, a 100 nm wide nanochannel becomes too large compared to the Debye screening length), this can be achieved by using atomic layer deposition (ALD) (see, Figure S5, Supporting Information, for channel width reduction by Al_2O_3 ALD). This combined HSQ/ALD approach is also promising for the development of vertical nanofluidics transistors as Pt can also be deposited by ALD (Figure S5, Supporting Information).

Figure 2 illustrates the versatility of the technique with three different substrates: silicon, glass slide, and flexible polyethylene naphthalate (PEN), respectively. On silicon substrates, in complement to Figure 1, Figure 2a–c shows the HSQ patterned area (150 μm large and 300 μm long), the PDMS-sealed nanofluidics channels before water filling, and the different stages of water filling under a given pressure of 3 bars. It confirms that nanofluidic channels can be successfully operated with this technology. Although difference in filling times (up to tens of seconds) is observed, all channels can be filled under the same pressure, which is consistent with the small dispersion in channel widths observed by SEM. The process can be transposed to other substrates by simply evaporating a thin layer of germanium (≈ 5 nm) on top of the HSQ before e-beam lithography so as to evacuate charges. After exposure, the germanium

layer was removed with a 1:1 solution of H_2O_2 : H_2O during ≈ 1 min. Then, the process is the same as on silicon. Figure 2d–f shows HSQ patterned areas on a glass slide and the complete filling of fluorescent marked DNA molecules inside the nanofluidic channels.^[14,25] Figure 2g,h indicates that similar results can also be obtained on a flexible substrate. In that particular case, the flexible substrate was first fixed on silicon substrate by means of a droplet of PDMS prior to spin-coating.

3. Fabrication of Nanochannels with Extremely Slow Evaporation Rate by Direct Prototyping of AZnLoF and HSQ Resists (Second Approach)

We also propose a second HSQ-based approach using the conventional sacrificial layer method (see Figure 3). These sacrificial layers are usually either metal layers^[26–30] or positive resists like poly methyl methacrylate (PMMA)^[8] and both of these have some drawbacks. The former one involves removal of metal layers at the end of the process which is usually difficult and requires few hours.^[31,32] The latter one involves writing of large areas, which is not the optimum solution.

Recently, it was demonstrated that AZnLoF, usually used for optical lithography, could also be patterned by e-beam with very high resolution.^[33] We spin-coated 100 nm thick diluted resist AZnLoF 2020 in PGMEA (1:3 ratio), and defined nanochannels

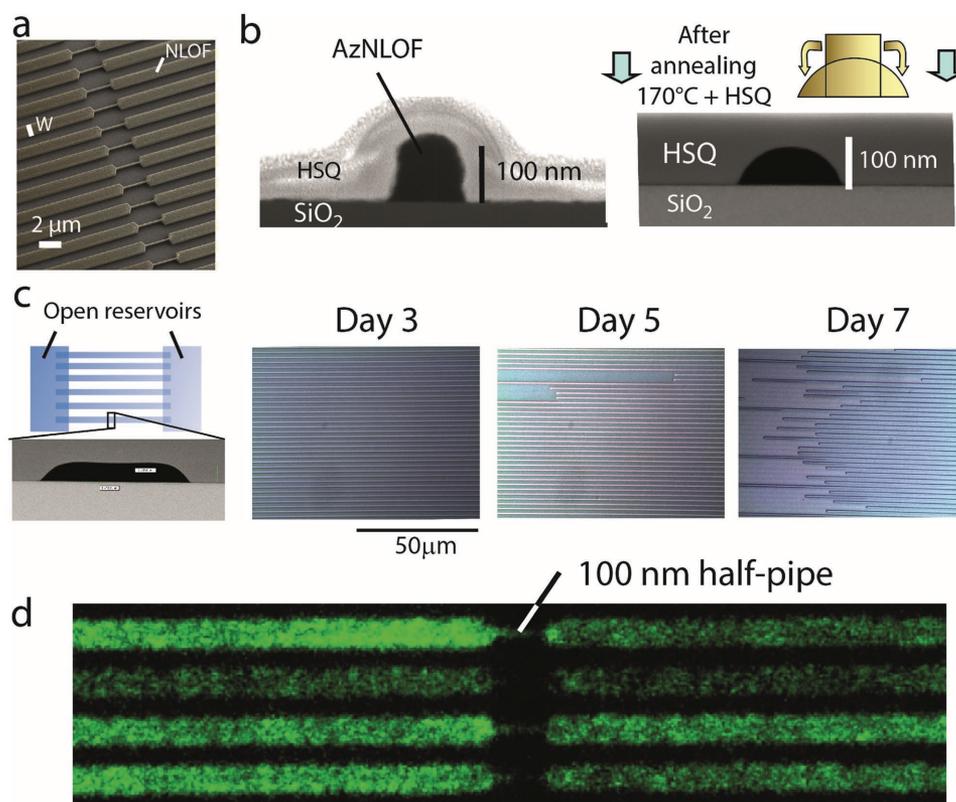


Figure 3. HSQ-based nanofluidics with sacrificial layer approach. a) SEM image (top view) of the AZnLof sacrificial layer. b) Cross-sectional SEM image of nanochannels before annealing (left) and after the complete process including sacrificial layer removal (right). c) Study of the evaporation rate. Left: Schematic view of the nanofluidic channels (3 μm wide, 100 nm thick) nanofluidic channels with open reservoirs. The related cross-section SEM image is shown below. Right: Optical images of the nanofluidic channels showing the extremely slow evaporation rate over one week ($<1 \text{ fL h}^{-1}$ starting after day 4). The pictures have not been taken on the structure center (slightly on the left) for showing evaporation coming only from the sides. d) Confocal microscope image of ss-DNA marked with Alexa fluorophore.

by e-beam, prior to HSQ deposition (Figure 3a). Both lateral and vertical dimensions can be set down to $\approx 100 \text{ nm}$ (Figure 3b). We noticed that an annealing step at $170 \text{ }^\circ\text{C}$ enabled optimization of the resist roughness leading to a perfect half-cylinder (Figure 3b). After HSQ deposition and baking, a piranha solution ($\text{H}_2\text{SO}_4/\text{H}_2\text{O}_2:2/1$) was used to dissolve and remove the embedded AZnLof resist by gentle agitation (50 rpm) for 30 min, leaving channels with clean and smooth inner surfaces. The remaining piranha in the channels was replaced finally by deionized (DI) water rinse under agitation. The advantages of HSQ to make the channel rather than other deposition methods such as sputtering, are to take advantage of its planarity and get rid of extra lithography and etching steps that can be complicated, in particular on patterned surfaces. This will be illustrated in the next section. We also experienced a difficulty for removing the AZnLof layer when it is covered with another oxide such as an Al_2O_3 deposited by ALD before HSQ deposition.

Interestingly, we noticed that water in the channels remained for at least one week without noticeable evaporation with inlets/outlets open to air as presented in Figure 3c (no PDMS bonding in this approach); whereas in the first approach with direct HSQ prototyping, the complete evaporation was observed in few minutes. These experiments have been performed in clean room at a fixed temperature of $21 \text{ }^\circ\text{C}$

and a humidity ratio of $\approx 50\%$. It was previously shown that roughness or section shape (square vs circular) plays a critical role in the evaporation in microfluidic channels,^[34–40] and consensus seems to have been reached that surface cleanliness plays an important role.^[41] However, typical evaporation times in micro- or nanofluidic channels are in the minute range^[34–42] (or in the range of mm min^{-1}), which is more than four orders of magnitude faster than in the present study. Sole consideration of the steady-state vapor diffusion governed by the Laplace equation^[43] should lead to evaporation times in the second or minute range with or without consideration of evaporation-induced cavitation effects.^[37] The present structure has a small roughness which may increase the evaporation time as already discussed in the literature,^[34] but cannot fully explain the extremely slow evaporation rate observed. A full understanding of the underlying mechanisms requires a dedicated experimental and theoretical study as in refs. [37] and [44]. We suggest that the experimentally observed slow evaporation rate could be related either to the absence of impurities due to non-exposure to air, or to a hydrophobic/hydrophilic transition at nanofluidic channels inlets/outlets^[45,46] (HSQ hydrophobicity depends on the e-beam dose^[22] which is expected to be weaker at channel ends), or eventually to nanoporosity in HSQ. This extremely slow evaporation rate is of practical interest to simplify setups for microscopy experiments (see Figure 3d

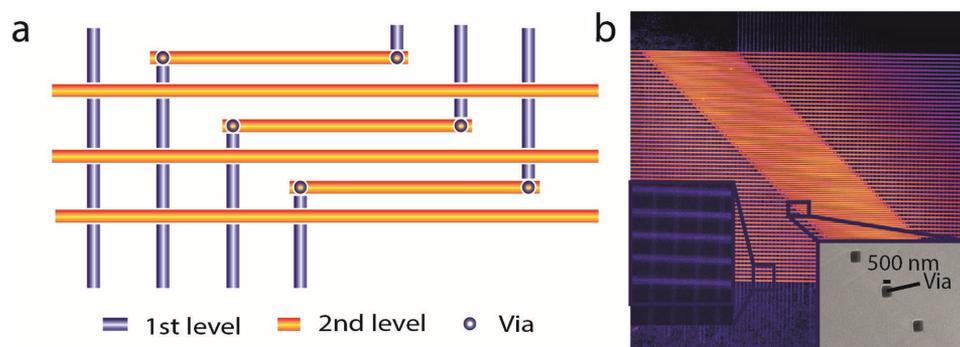


Figure 4. 3D HSQ-based nanofluidics. a) Schematic representation of the 3D nanofluidics structure. b) Related confocal microscope image of the 3D nanofluidics channels with ss-DNA marked with Alexa fluorophore. Insets: SEM image of the vias used for the 3D nanofluidics channels.

showing a fluorescent image of nanofluidic channels filled with marked DNA).

4. 3D Nanofluidics and Nanometric Precision Alignment

In a similar manner as for the fabrication of photonic crystals,^[20] the good alignment ability and the unique planarity provided by HSQ, enable to stack these layers together to make 3D nanofluidic channels (Figure 4a,b), a key step for highly integrated and multiplexed nanofluidics. It was achieved here by repeated AZnLof patterning and HSQ deposition. Figure 4b, inset, shows two levels of channels with interconnect openings (vias) between the top and bottom layers, as well as the planar upper surface of the HSQ layer (images of the various steps are shown in Figure S6, Supporting Information). Vias were achieved by performing e-beam lithography on PMMA resist (5% 950K, 3 μm thick) prior to reactive ion etching (RIE) of HSQ by CF_4/CHF_3 plasma. Several design rules have to be respected for HSQ-based 3D nanofluidics. A thin HSQ layer is required because of the nonuniform reactive plasma etching rate between vias centers and sides, which renders difficult the uniform etching down to the 100 nm thick underlayer of AZnLof (Figure S7a,b, Supporting Information). An optimum HSQ-layer thickness (≈ 500 nm) was selected based on the observation of nanofluidic channels collapse. This collapse happens either during HSQ baking or during AZnLof removal for HSQ layer thicknesses below 400 nm (Figure S7b, Supporting Information), and it is channel-width dependent. In particular, for channels wider than 4 μm , channels are systematically collapsed, independently on the HSQ layer thickness. In the 500 nm thick HSQ layer configuration, 500 nm wide vias could be achieved. Smaller vias (e.g., 200 nm or smaller), require significant optimization of the etching rate and suffer from reproducibility. Larger vias affect the uniformity of the second layer of AZnLof that partly falls into the via (Figure S7, Supporting Information), and results in a clogged hole after the deposition of a second layer of HSQ. With these design rules in mind, the process is very robust and reproducible.

Although some previous reports have successfully shown the possibility of 3D nanofluidics,^[47–49] the degree of

control/precision proposed with HSQ-based nanofluidics (channels crossing) brings new perspectives toward well-controlled large-scale integrated nanofluidics or hybrid devices. In addition, when the surface is initially patterned, the combination of nanometric precision alignment and planarity is required. For example, single-crystal Au nanoparticles (fabricated by e-beam lithography and thermal annealing), or nanoscale transistor sensors, have been successfully used in the fields of molecular electronics^[50–53] electrochemistry^[53,54] and single-molecule or single-charge-sensitive biosensors.^[55–57] Nanofluidics could be a very attractive approach to provide an upper electrode made of liquid metal^[58] for high-frequency molecular electronics,^[53] to reduce parasitic capacitance in nanoelectrochemistry, or simply to focus analytes on top of nanoscale biosensors. Figure 5 shows the ability to fabricate such structures on Au nanoparticles and 50 nm thick nanoscale transistor biosensors without any further complexity arising from the patterned surfaces.

5. Conclusion

We proposed the demonstration of a well-controlled and versatile technique for the fabrication of nanofluidic channels with nanometric precision alignment based on HSQ. The first approach requires only a single, small area writing step, and enables nanometric precision alignment. In the second approach, a conventional sacrificial layer approach was exploited for the fabrication of ≈ 100 nm diameter half-pipe HSQ nanofluidics channels together with nanometric precision alignment and 3D nanofluidics demonstration. The proposed reliable approaches provide a pathway for the development of more and more complex nanofluidic systems including the interfacing of nanofluidics with nanoscale sensors, while the extremely slow evaporation rate brings simplicity for the characterizations or applications, and new perspectives for basic research in nanofluidics.

6. Experimental Section

Si Mold: S1818 resist (Microposit) was spin-coated at 2500 rpm with an acceleration of 1000 rpm for 12 s. Silicon was etched using RIE. The gases used for the RIE process was SF_6 and O_2 and the gas used for

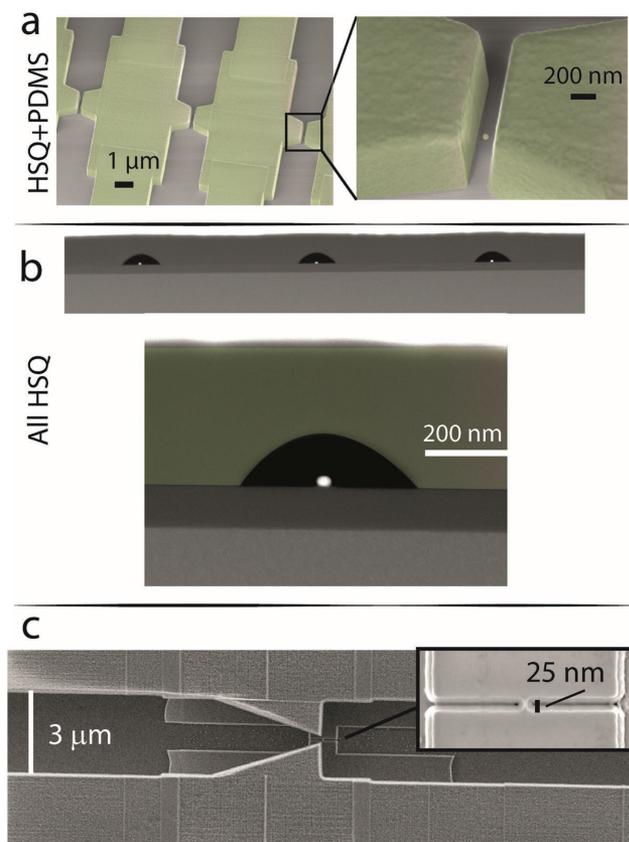


Figure 5. Nanochannels with nanometric alignment precision ability. a) SEM image of the nanofluidics channels aligned on 20 nm large gold nanodot (fabricated with the direct patterning of HSQ). b) SEM cross-section images of nanofluidics channels fabricated with the sacrificial layer approach with precisely aligned gold nanoparticles. c) SEM top view of an HSQ-based nanofluidic channel aligned on a patterned surface, namely, a nanoscale transistor biosensor. Inset: SEM image of the nanoscale transistor biosensor.

passivation was C_4F_8 . The flow rates of SF_6 , O_2 , and C_4F_8 were 450, 45, and 100 standard $cm^3 \text{ min}^{-1}$ (sccm), respectively. The coil power was 1000 W. RIE had an approximate etch rate of $4 \mu\text{m min}^{-1}$. After etching the wafers, the microstructures on the wafers were examined by optical microscopy. The remaining photoresist was stripped from the silicon wafer by immersing the wafer in EKC 265 at 60°C for 30 min, followed by immersion in acetone and isopropanol for 5 min each.

PDMS Layer Fabrication (for Method 1): A thin layer of PDMS (thickness: 200 μm) was required for the alignment protocol and for precisely defining the holes for tubing. The uncured PDMS (mixing ratio of curing agent/base ratio: 1/3) was spin-coated at 300 rpm for 30 s, with an acceleration rate of 100 rpm s^{-1} . It was then cured in two steps: (i) at 65°C for 20 min in contact with a hot plate and (ii) in a convection oven at 120°C for 40 min.

Alignment and Bonding (for Method 1): Prior to bonding, the Si chip and PDMS layer were exposed to O_2 plasma (120 W, 0.7 mbar, 180 s) and to UV-ozone for 5 min. After proper alignment and bonding with $1 \mu\text{m}$ alignment precision,^[24] thermal annealing was performed at 120°C for 60 min.

A second layer of PDMS (2 mm in thickness) was used to guide the tube and reduce the mechanical stress at the inlet and outlet. Access holes for connecting the inlet and outlet tubes (PTFE tubing: 0.7 mm/0.3 mm outer diameter [OD]/inner diameter [ID]) were cored into the 2 mm thick PDMS by using a 300 μm ID needle, with an approximate distance between the two holes of less than 1 mm.

Supporting Information

Supporting Information is available from the Wiley Online Library or from the author.

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