Design of Converters for PV System under Partial Shading Conditions

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	ADSTRACT				
Article history:	This paper presents a maximum power generation with the interconnection of photovoltaic modules under partially shaded and/or mismatching conditions. The partial shading condition reduces power level of each module. The reduction in power due to the partial shading will be compensated by the bidirectional converter. The proposed system consisting of two and three PV modules connected in series under partial shading conditions which are				
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Keyword:	capable of increasing the power levels up to 50% compared to conventional by-pass diode structure. In general 'n' number of modules connected in				
Bi-directional Cuk Converter MPPT Partial Shading PV System	series so that the maximum power gain will be expected to (100/n) %. This is achieved by developing the new control strategy in which the correct adjustment of converter duty ratio under partially shading conditions. The novel control scheme is developed by using analysis of the power converters. The proposed scheme was verified in MATLAB/SIMULINK.				
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1. INTRODUCTION

The depletion of traditional energies and the increase in pollution and greenhouse gases emission, renewable energy contribution makes vital role in energy sources [1, 2]. Some of the renewable energy sources are hydro, solar and wind. Of these sources of energy, with corresponding increase in cost of solar photovoltaic energy offers a prominent alternative renewable energy source. Solar energy is free, abundant, pollution free and also distributed throughout the earth [3] with only drawback of initial installation cost. Photovoltaic energy sources have been established for space applications where cost is not a primary consideration. With the present trend of research, cost of photovoltaic cell is expected to go down substantially in future. Because of these tremendous usages of solar energy, photovoltaic cells invade generation of power in houses, intermediate-size commercial installations, or large central power stations. In house hold applications photovoltaic system of kilowatts in size is mounted on the roof top. The available DC power, which varies with solar insolation and temperature, is converter to single phase 50-Hz AC fed to utility line [4, 5].

Now-a-days there are so many research topics related to the photovoltaic generation. As the solar power generation is accepted through out by all, the themes of PV system is to control approaches for maximum power point tracking, this topic determined by the V-I or P-V [6] characteristic curves of the solar panel. Uniform solar power-voltage characteristics of PV array are maximized by Maximum Power Point Technique (MPPT) [7]. Different MPPT algorithms have been proposed in previous literatures, such as perturbation and observation [8, 9], hill climbing, incremental inductance [10, 11] (INC) etc.

As the weather conditions are intermittent and uncertain using MPPT can set and system working point to the optimum and extract available maximum power from PV [12]. Typical photovoltaic installations, PV arrays are formed by connecting multiple PV modules in various configurations such as series [13],

parallel, series-parallel, etc. A by-pass diode or by-pass switch is connected in parallel with each PV module for the protection of solar cells against efficiency degradation and hot spot failure effects. In the case where one or more of the PV modules comprising the PV array shaded [14, 15] i.e. due to dust, shading from surrounding buildings, trees or poles, non-uniform solar irradiation then the PV array exhibits multiple local maxima and only one of them corresponds to the global MPP.

The connection of PV cells and modules in parallel, in order to avoid the effect of partial shading is applicable only in low-power PV systems. Under partial shading conditions, the conventional MPPT techniques fail to guarantee successful [16, 17] tracking of the global MPP, resulting in significant reduction of both power generation and the PV energy production system reliability. In this paper, bidirectional converter [18] with two or three PV modules is proposed instead of individual PV module for each DC-DC converter to reduce losses due to converter, cost, and size. The integration of PV module-converter unit known as a Module-Integrated PV and Converter (MIPC) [19].

This paper presents a new control scheme of bidirectional converters connected to the two and three PV modules can be raised to voltage and current levels under partial shading conditions.

2. DESIGN OF PV ARRAY UNDER PARTIAL SHADING CONDITIONS

A partially shaded module can be modelled by two or three groups of PV cells connected in series inside a module. During partial shading condition, a fraction of the each PV cells which receive different level of irradiance still operate at the normal efficiency. As the current flow through every cell in a series configuration is naturally constant, the shaded shell is operating with a reverse bias voltage to provide the same currents⁶. The resulting reverse polarity leads to power consumption and a reduction in the maximum output power of the partially shaded PV module. The Figure 1 Shows an equivalent circuit representation of Photovoltaic cell under partial shading conditions.



Figure 1. Three PV Modules Connected in Series

The mathematical expression for a single Photovoltaic cell output current is given by equation(1).

$$I = I_{PH} - I_S \left[exp\left(\frac{qV}{kT_CA}\right) - 1 \right]$$
⁽¹⁾

The series configuration of three PV modules in an array receiving irradiance levels of G_1 , G_2 and G_3 respectively. Under the uneven insolation G_1 , G_2 and G_3 are assumed to be irradiation levels like (1000w/m², 600w/m², and 200w/m²) etc. The presence of taking a taking single bypass diode for each PV module into consideration, the output current and output voltage at the array terminal can be obtained by solving the following equations (2):

$$V_{PV} = \begin{cases} V_{P1} \ I_{PV} > I_{PV1} \\ V_{P2} + V_{P1} \ I_{PV} > I_{PV2} \\ V_{P3} + V_{P2} + V_{P1} I_{PV} > I_{PV3} \end{cases}$$
(2)

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Applying equation (2) for the current and voltage for the entire PV array the results represented in figure 2(a) and 2(b) are obtained. The Figure 2(a) and Figure 2 (b) shows the I-V and P-V characteristics of the three PV modules under partial shading conditions $(1000\text{w/m}^2, 600\text{w/m}^2, \text{and } 400\text{w/m}^2)$.







Figure 2(b). PV Characteristics pf PV Panel under Partial Shading

3. PROPOSED CONVERTERS FOR MODULE INTEGRATED PV UNDER PARTIAL SHADING CONDITIONS

Many schemes have been developed in which each individual PV module is provided with a DC–DC converter forms an integrated PV module-converter unit which is known as a module-integrated PV and converter [18] (MIPC). But, the key problem with this cascaded MIPC scheme is, it may not enable individual PV modules to achieve maximum power point (MPP) operation in the partial shading conditions. The Figure 3shows an alternative scheme based on bypassing MIPCs. In this configuration the MIPCs are connected in series but PV modules are grouped in pairs and each pair is connected to a bidirectional DC-DC converters such Cuk and buck-boost converters.



Figure 3. Configurations of MIPC System Using Bypass Approach

In this configuration the MIPCs are connected in series but PV modules are grouped in pairs and each pair is connected to bidirectional DC-DC converters such as Cuk and buck-boost converters. The main advantage of this scheme over the cascaded approach is that the passes power flow under partial shading conditions i.e. different illumination levels. The performance of this scheme has been shown to be much better than the cascaded system. The MPP tracking for the bypassed MIPC can still be based on the schemes which have been widely applied under none partially shaded schemes. Most of these algorithms are only used for one peak point. Hence it requires less computational effort and are simpler to implement comparing to those applied in bypass diode and cascaded MIPC systems.

3.1. TWO PV MODULE SYSTEMS

This paper presents a novel model based control scheme for a particular module integrated PV and converter system, where the bidirectional DC-DC converters are used as the bypass converters²⁰. The Figure 4shows the MIPC system using bypass for two PV modules and such a system connected to a load through a terminal boost converter. This scheme consisting of two serially connected PV modules and a bidirectional

converter having one end connected to PV_1 and other end is connected to PV_2 and step up converter is used for output power conditioning.

When both the photovoltaic modules are uniformly illuminated the bidirectional converter produces zero power. At this time both switches S_{11} and S_{21} are turned off and current direction is passing only through PV_2 as well as PV_1 to the load. Hence the total power is supplied to the load is given in equation (3).

$$P_T = V_T I_T = (V_{P1} + V_{P2}) I_T = V_{P1} I_{P1} + V_{P2}$$
(3)

And

$$I_{\rm T} = I_{\rm P1} = I_{\rm P2}$$
 (4)

Where V_T is the terminal voltage, I_T is the terminal current, V_{P1} and I_{P1} are the voltage and current of PV_1 at the maximum power point where as V_{P2} and I_{P2} are the voltage and current of PV_2 .



Figure 4. MIPC System Using Bypass Approach Two PV Modules

If the illumination levels of two modules i.e. PV_1 and PV_2 are different, for example the module PV_2 is shaded (i.e. low illumination) the power output from module PV_1 is more than the module PV_2 . By making the switch pair S_{11} and D_{21} is in active position i.e. switching at a fixed frequency with duty ratio K_{11} , the converter can mix the power from PV_1 away from passing through PV_2 . The voltage across PV_2 relates to that of PV_1 is given by:

$$V_{PV2} = V_{PV1} \tag{5}$$

And the current bypass to the bidirectional converter is given as:

$$I_{L11} = (I_T - I_{P2}) \left(\frac{K_{11}}{1 - K_{11}}\right) \tag{6}$$

The terminal current is given as:

$$I_T = I_{P1} - (I_T - I_{P2}) \left(\frac{\kappa_{11}}{1 - \kappa_{11}}\right)$$
(7)

$$= I_{P_1}(1 - K_{11}) + I_{P_2}K_{11}$$
(7) (8)

So the power output to the load without considering the losses is given as:

$$P_T = V_T I_T = \left(V_{P1} + V_{P2} \left(\frac{K_{11}}{1 - K_{11}} \right) \right)$$
(9)

$$= V_{P1}I_T + V_{P1}\left(\frac{K_{11}}{1 - K_{11}}\right)I_{P2}(8)$$
(10)

The above analysis shows that by varying the duty ratio K_{11} in equation (8), the maximum power extraction can be achieved by shaded module. The duty ratio $K_{11} = 0$ (i.e. D_{21} is continuously forward biased), the output power of the system is $P_T = V_{PI}I_{PI}$, so the shaded module is totally bypassed and producing zero power. The duty ratio is varying between 0 and 0.5. Similarly this analysis can be applied to the case when PV1 is shaded and switch pair S21 and D11 is in active position, the duty ratio K21 can be adjusted in the range between 0-0.5 for mixing the power to the converter. The Table 1 shows the comparison of maximum power extracted by using bypass diode and that using MIPC two PV module system.

3.2. THREE PV MODULE SYSTEMS

The Figure 5 shows the MIPC system using bypass for three PV modules and such a system connected to a load through a terminal boost converter. This scheme consisting of three serially connected PV modules and two bidirectional converters¹⁹ having oneend connected to PV₁ and other end connected to PV₃ and step up converter is used for output power conditioning.



Figure 5.MIPC System Using Bypass Approach Three PV Modules

As can see Figure 5, there are total four device pairs these are $S11-D_{21}$, $S_{12}-D_{11}$ for bidirectional converter 1, and S_{21} - D_{22} , S_{22} - D_{21} for bidirectional converter 2. The system can be operated in four modes as listed shown in below.

- $\begin{array}{ll} 1. & S_{11}-D_{12} \ with \ S_{21}-D_{22} \ , \\ 2. & S_{11}-D_{12} \ with \ S_{22}-D_{21} \ , \\ 3. & S_{12}-D_{11} \ with \ S_{21}-D_{22} \ , \\ 4. & S_{12}-D_{11} \ with \ S_{22}-D_{21}. \end{array}$

Following the same approach as in the previous section for a two PV module system, the total output power equation for a three PV module system is given as:

$$P_T = P_1 + P_2 + P_3 = (V_{P1} + V_{P2} + V_{P3})I_T$$
(11)

$$= V_{P1}I_{P1} + V_{P2}I_{P2} + V_{P3}I_{P3}$$
(12)

When the illumination levels for *modules* PV_1 and PV_2 are different, the power is bypassed to or from converter 1. The current I_{L1} is non-zero and when it is positive.

$$I_{L1} = I_{P1} - I_T > 0 \tag{13}$$

The equation (10) can be expressed in terms of power and voltages as:

Table 1. Comparison of Maximum Power Extracted						
Using Bypass Diode and That Using MIPC System						
S.NO	Solar irradiation	Solar irradiation	Total Output power	Power extracted	Gain ∆P	Percentage gain
	for PV1 (%)	for PV2 (%)	extracted P _T (W)	using bypass diode	$= P_T - P_d(W)$	$\left(\frac{\Delta P}{1000}\right)$
				$P_D(W)$		$\left(\frac{\overline{P_d}}{P_d} * 100\%\right)$
1	1000	1000	59.5	59.5	0	0
2	1000	800	57.75	25.2	32.55	129.16
3	1000	600	54.45	19.8	34.65	175
4	1000	400	46.75	10.8	35.95	332
5	1000	200	39.71	3.6	36.11	1003.05

Table 1. Comparison of Maximum Power Extracted

Table 2.Comparison between the Amounts of Powers Extracted Using PV-Integrated Converter and Those Using Bypass Diodes

		0	0		0 71		
S.NO	Solar irradiation for PV1 (%)	Solar irradiation for PV2 (%)	Solar irradiation for PV3 (%)	Total Output power extracted P _T (W)	Power extracted using bypass diode P _D (W)	$Gain \Delta P = P_T - P_d(W)$	Percentage gain $\left(\frac{\Delta P}{P_d} * 100\%\right)$
1	200	600	1000	63.7	23.7	36	129.96
2	400	600	1000	71.7	35.7	36	100
3	700	600	1000	81.8	45.8	36	78.6
4	1000	600	1000	93.2	93.2	36	62.93
5	200	300	1000	51.9	51.9	36	226
6	400	300	1000	59.9	59.9	36	150.6
7	700	300	1000	70	70	36	94.4
8	1000	300	1000	81.4	81.4	36	79.2

$$\frac{V_{P_1} + V_{P_2} + V_{P_3}}{V_{P_1}} > \frac{P_1 + P_2 + P_3}{P_1} \tag{14}$$

The equation (11) can be expressed as:

$$P_1 + P_2 + P_3 < 3P_1 \tag{15}$$

The equation (12) can be expressed in terms of irradiations G₁, G₂, G₃ as:

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$$G_1 + G_2 + G_3 < 3G_1 \tag{16}$$

The equation (13) can be written as:

$$\frac{G_2+G_3}{2} < G_1$$
 (17)

Thus for specific set of the light levels, if they satisfy the equation (14), switch pair S₁₁-D₂₁ should be active to ensure positive direction of I_{L1} , otherwise the switch pair S_{21} - D_{21} is activated. The similar analysis can be applied to converter 2 but now it is based on the current direction of IL22, so switch pair S22- D_{21} is active if the light condition is given as:

$$\frac{G_1+G_2}{2} < G_3$$
 (18)

The combination of light levels and their respective switching status for the two inner converters 1 and 2 are explained in Table 3.

Table 3. Light Conditions to Determine the Active Pairs				
Mode	Light conditions	Active device pairs		
1	$\frac{G_2+G_3}{2} < G_1 \text{ and } \frac{G_1+G_2}{2} < G_3$	S_{11} - D_{21} and S_{22} - D_{12}		
2	$\frac{G_2+G_3}{2} > G_1 \text{ and } \frac{G_1+G_2}{2} > G_3$	S_{21} - D_{11} and S_{12} - D_{22}		
3	$\frac{G_2+G_3}{2} < G_1 \text{ and } \frac{G_1+G_2}{2} > G_3$	S_{11} - D_{21} and S_{12} - D_{22}		
4	$\frac{G_2 + G_3}{2} > G_1 \text{ and } \frac{G_1 + G_2}{2} < G_3$	S_{21} - D_{11} and S_{22} - D_{12}		

The Figure 6 (a, b, c, d) shows the four modes of switching operations.





Figure 6 (a). Mode-1: S_{11} - D_{21} and S_{22} - D_{12} Are Active Figure 6 (b). Mode-2: S_{21} - D_{11} and S_{12} - D_{22} Are Active



Figure 6 (c). Mode-3: S_{11} - D_{21} and S_{12} - D_{22} Are Active Figure 6 (d). Mode-4: S_{21} - D_{11} and S_{22} - D_{12} Are Active

CLOSED LOOP CONTROL OF THREE PV MODULE SYSTEMS UNDER PARTIAL 4. SHADING CONDITION

The regulation of the converter operation in Section3 was achieved through open-loop controllers, it is highly desirable to have a complete closed-loop control in a practical system which has the potential to eliminate any steady-state errors due to power losses in converter and also prevents any fluctuation of PV voltage and current due to sudden or rapid change in weather conditions. In addition, the control scheme for this system should also enable all three PV modules to operate at their peak power points for any illumination conditions.

All these require coordinated control for the two inner bidirectional converters and the terminal boost converter. A two loop scheme is thus proposed, which consists of a control algorithm for the inner converters adjusting the terminal voltages of individual PV modules and a feedback control scheme to regulate the entire system output voltage by the terminal boost converter. The whole system is shown in Figure 7.



Figure 7. Configuration of the Overall System for A Three PV-module system

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4.1. CONTROL ALGORITHM FOR INNER CONVERTERS

According to the measured levels of sunlight and shading conditions within each unit, the switching modes for the two inner converters can be selected based on the scheme described in Section 2.1. Subsequently the duty ratios of the converters are adjusted according to a specially designed control algorithm. For a typical dc-dc converter the voltage on one side, either the input or output side, is held constant by a voltage source and the other side is controllable. However, in this system the voltages at both sides of the inner converter are determined by their respective PV modules which are varying simultaneously when changing the duty ratio for each MIPC unit shown in Figure 6(a, b, c, d). A block diagram of the control scheme is as shown in Figure 8. Here V_{upper} is the main controlled voltage to determine the duty ratio k_1 and the controller $H_1(s)$ is set as a lead compensator. The signal from the 'Detuning Loop' is treated as a disturbance to adjust K_{upper}. In this way, tight control of V_{upper} can be achieved through the 'Main Loop' in Figure 8.Whilst variation of V_{lower} may be contained through the 'Detuning Loop' in Figure 8. Similar analysis as above can be done for the regulation scheme of K_{lower} , which is the control variable for the duty ratio of the MIPC's lower switch but now V_{lower} is taken as the main controlled voltage while V_{upper} as disturbance. Nevertheless for stability, either the regulation of K_{lower} or K_{upper} (but not both) can be done at a time; this condition is still valid as from Section 2, it is known that one device pair in each MIPC can only be active. The overall design scheme can then be applied to both MIPC units in Figure 6(a, b, c, d). Tuning of the lead compensator is based on the transfer functions in (16) and (24). The system is stable, having four poles and two zeros in the left-hand-side of s-plane. The terminal voltage is varying inversely with the duty ratio as indicated by the negative sign. The controller is designed to increase the phase margin through phase lead compensation and to realise zero steady state error using the lag term. This can be analysed through its two derived transfer functions is shown in Figure 8.



Figure 8. Closed Loop Control Scheme for Controlling the PV Terminal Voltages

The first is between the PV voltage at its upper terminal (which is denoted as V_{upper}) and the control variable whichcontinuously adjusts the duty ratio for the upper switch (which is denoted as K_{upper}); this is given as¹⁹:

$$G_1(S) = \frac{V_{uppr}(S)}{K_{upper}(s)}$$
(16)

$$= -\frac{\beta_1 S + \beta_0}{\alpha_3 S^3 + \alpha_2 S^2 + \alpha_1 S + \alpha_0} V_{\mathrm{T}}$$
⁽¹⁷⁾

Where $\beta_1, \beta_0, \alpha_3, \alpha_2, \alpha_0$ are the parameters of circuit elements inductor and capacitor. The above parameters can express in terms of capacitance and inductances.

$$\beta_1 = C_n + CK + \frac{L(1-K)}{R_{P2}} \left(\frac{(1-K)}{R_{P1}} - \frac{K}{R_{P2}} \right)$$
(18)

$$\beta_0 = \frac{(1-K)^2}{R_{P1}} + \frac{K^2}{R_{P2}} \tag{19}$$

$$\alpha_3 = c_n L \left(2C + \frac{L}{R_{P_1} R_{P_2}} \right) + c^2 L \left(K^2 + (1 - K^2) \right)$$
(20)

$$\alpha_{2} = L(C_{n} + CK^{2} + C(1 - K^{2}))\left(\frac{1}{R_{P_{1}}} + \frac{1}{R_{P_{2}}}\right)$$
(21)

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$$\alpha_1 = C_n + (K^2 + (1 - K^2)) \left(C + \frac{L}{R_{P_1}R_{P_2}}\right)$$
(22)

$$\alpha_0 = \frac{((1-K)^2)}{R_{P1}} + \frac{K^2}{R_{P2}}$$
(23)

The second transfer function i.e. equation (4.8) gives lower terminal voltage and lower switch duty ratio.

$$G_2(S) = \frac{V_{lower}(S)}{\kappa_{lower}(S)}$$
(24)

$$=\frac{\gamma_1 S+\gamma_0}{\alpha_3 S^3+\alpha_2 S^2+\alpha_1 S+\alpha_0} V_{\rm T}$$
⁽²⁵⁾

Where γ_1 and γ_0 are the parameters of circuit elements inductor and capacitor. The above parameters can express in terms of capacitance and inductances.

$$\gamma_1 = C_n + C(1 - K) + \frac{LK}{R_{P_1}} \left(\frac{K}{R_{P_2}} - \frac{(1 - K)}{R_{P_1}} \right)$$
(26)

$$\gamma_0 = \frac{(1-K)^2}{R_{\rm P1}} + \frac{K^2}{R_{\rm P2}} \tag{27}$$

Note that the two transfer functions also vary according to PV module's internal impedances, steady-state duty ratio values, as well as terminal voltage V_T , which all take different values according to the operating point. Thus they both are nonlinear processes. Taking into account the effect of duty ratio change on the two terminal voltages, the closed-loop control scheme for the inner converter is so designed that control of one terminal voltage takes precedence in determining the converter duty ratio. The other, however, is treated as a disturbance signal to detune the control signal.

5. RESULTS AND ANALYSIS

The performance of the PV-converter integrated system has been evaluated through computer simulation. The model for the two and three PV module systems have been developed using MATLAB-SIMULINK software package including SIMPOWER tool boxes. The lead compensator controller for both inner and outer converters is modelled by using control tool box MATLAB. A MATLAB algorithm for MPP tracking is also incorporated into the model through an under-defined s-function block.

The open loop system can be operated in two ways one is two PV module systems and other is three PV module systems. The output of two and three PV modules can be connected to dc load with the help of boost converter. The Figure 9 displays the simulated response for the two PV modules with two different partial shading conditions. All PV modules are operating under the same temperature of 250C and initially, they are uniformly irradiated with G1 = G2 = 1000W/m2. The effects on the PV voltages and powers resulting from the changes of light levels G1 = 1000W/m2 and G2 = 400W/m2. At t = 0.51s, the voltage levels are sharing between two modules and gives maximum voltage level. Due to changes in irradiation levels the voltage levels are decreasing up to 40V and maintain constant level starts at time t = 1 s.





Figure 9. Simulation Output Voltage, Current, Power of MIPC System Using Bypass Approach for Two PV Modules

Figure 10. Simulation Output Voltage, Current, Power of MIPC System Using Bypass Approach for Three PV Module

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Figure 10 displays the simulated response for the three PV modules with three different partial shading conditions i.e. $(1000W/m^2, 500W/m^2, and 800W/m^2)$. The effects on the PV voltages and powers resulting from the changes of light levels $G_1 = 1000W/m^2$, $G_2 = 500W/m^2$ and $G_3 = 800W/m^2$. At t = 0.8s, the voltage levels are starts rising and increased to maximum voltage level 68.9V at t = 0.95s. Due to the changes in irradiation levels the voltage levels are decreasing up to 60V and maintain constant level starts at time t = 1 s.

The proposed control system is considered robust in responding to large variation of weather conditions. The simulation has been repeated for the Perturbation-and-Observation (P&O) tracking method and the amount of power extracted is found to be consistent with that for the model-based approach. The output voltage of the three PV module system is connected to MPPT is shown in Figure 11. The Output voltage contains small oscillations, these can be reduced by using compensator, and here lead compensator is used.

The step response of the above transfer function is shown in Figure 12. In Figure 12 with and without compensator responses are compared.





Figure 11.Output Voltage of MPPT connected to MIPC system Using bypass approach for three PV modules

Figure 12.Step Response of With and Without Compensator

Now the three PV module systems, MPPT and compensator responses are connected to DC load with the help of boost converter.

The total output voltage of the configuration of overall system for a three PV-module system as shown in Figure 13. The output voltage is 59.5V.



Figure 13.Output Voltage of the Configuration of Overall System for a Three PV-Module System

The performance of this system is compared to the conventional system using only bypass-diode connection. To implement the later, the MPP control algorithm proposed can be applied. This method searches iteratively the maximum power point among the multiple power peaks. The simulated responses are shown in Figure 13. It can be observed that the major downside for the conventional system is the terminal

voltage fluctuation of within 10–59 volts range for every change of weather conditions. On the other hand, MIPC has substantially improved the system performance by only requiring a narrower operating voltage range. In addition, the extractable power output can be significantly increased by as much as 50%, through keeping all PV modules in optimal operation. This occurs when one of the three PV modules or a third of the total PV generating system is shaded. Using the integrated converter scheme, further power increment can be obtained for other shading levels. This is shown in Figure 13 from which two observations can be made; (1) the maximum amount of the power increment increases with the number of shaded PV modules and (2) the shading level, at which the peak point for the power increment is located, decreases with the number of shaded PV modules.

6. CONCLUSION

In this paper a newly developed control scheme of a module integrated PV and converter assembly for achieving maximum power generation in PV system under partially shading conditions. The structure can be developed with the help of two and three PV modules connected in series under various partial shading conditions. The proposed system increases the power levels as much as 50% compared to conventional bypass diode structure.

The control schemes were optimized using analytical equations for the transfer functions of the power converters, and the dynamic and stable response of the system can be obtained by using Perturbation and Observation method and Lead compensator methods. The proposed control schemes have been presently tested for PV modules under uniformly shading conditions.Provide a statement that what is expected, as stated in the "Introduction" chapter can ultimately result in "Results and Discussion" chapter, so there is compatibility. Moreover, it can also be added the prospect of the development of research results and application prospects of further studies into the next (based on result and discussion).

SPECIFICATIONS

Table 4. Specifications of PV Module

S.no	Characteristics	Specifications
1	Rated power	36W
2	Peak power	36W
3	Peak power voltage	16.5V
4	Open circuit voltage	21.4V
5	Short circuit current	2.3A

Table 5. Speciations of the Inner Converter Elements Resistor Inductor Capacitor and Duty Ratios

Resistor, inductor, Capacitor, and Duty Ratios.		
Parameter	Specification	
Capacitor	10uF	
Inductor	8mH	
Resistors	1 ohm	
Duty ratio	0.5	
Terminal voltage	U=48V, L=16V	

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