Research Article

Anti-windup compensator design for power system subjected to time-delay and actuator saturation

Maddela Chinna Obaiah¹ , Bidyadhar Subudhi¹

¹Department of Electrical Engineering, National Institute of Technology, Rourkela, India E-mail: chinnaobaiah.m@gmail.com

Abstract: In this study, a delay-dependent anti-windup compensator (AWC) is designed for supplementary damping control (SDC) of flexible AC transmission system (FACTS) device to enhance the damping of inter-area oscillations of the power system subjected to time-delay and actuator saturation. By employing global signal measurements, an SDC of FACTS device is designed without considering the effect of time-delay and actuator saturation to stabilise the power system using a robust output feedback controller with pole placement approach. Then, based on the generalised sector condition and Lyapunov–Krasovskii functional, an add-on delay-dependent AWC is designed to mitigate the adverse effect of time-delay and actuator saturation non-linearity. For the design of delay-dependent AWC, sufficient conditions guarantee the asymptotic stability of the closed-loop power system are formulated in the form of linear matrix inequalities (LMIs). These conditions are cast into the LMI-based convex optimisation problem to compute the AWC gains. To evaluate the effectiveness of the proposed controller, non-linear simulations were performed first using MATLAB/Simulink. Then, the authors implemented the proposed controller in real-time using the Opal-RT digital simulator. From the obtained results, it is observed that the proposed controller enhances the damping of inter-area oscillations by compensating the effect of time delay and actuator saturation.

1 Introduction

Inter-area oscillations with frequency range 0.2-0.8 Hz are one of the main concerns of the power system operators which are resulted when the two or more coherency groups of generators swing against each other [1]. The poor damping of these oscillations reduces the power transfer capacity of the transmission lines between the areas and even causes the system instability. Power system stabilisers (PSSs) [2-4] and flexible AC transmission system (FACTS) [5-7] with supplementary damping control (SDC) based on wide-area measurement systems provide sufficient damping to the inter-area oscillations by using remote feedback signals [8]. These signals from phasor measurement units to the control centre and control centre to actuators are transferred through a packet-based communication network. The usage of a communication network in the feedback loop introduces new problems such as time-delays which deteriorate the control performance or may even cause the closed-loop system instability [9]. The duration of time delays depends on the transferred distance, type of the communication network and protocol used for transmission. Therefore, it is important to consider the time delays in the process of damping controller design and find the time delay margin under which wide-area power system remains stable. Various damping controller design techniques are reported in the literature to guarantee the stability of the system by compensating the effect of time-delays [2, 4, 6, 10–12]. In [13–16], the damping controllers are proposed to compensate the time-delays that occur in the feedback loop based on model predictive control strategy.

On the other hand, actuator saturation is an inevitable nonlinear phenomenon in a control system due to the physical limitations or safety reasons; the actuator output signal is limited by its minimum and maximum values. The presence of actuator saturation leads to degradation of performance of the closed-loop system or even makes it unstable [17–19]. Several approaches have been proposed in the literature to address the stability of the system under actuator saturation [20–22], e.g. direct approach and indirect approach. In the direct approach, the actuator saturation is considered at the beginning of control design process [20], while in the indirect approach, a conventional controller designed first without considering saturation and then an add-on anti-windup compensator (AWC) is designed to minimise the adverse effect caused by the actuator saturation non-linearity [21, 22]. Among these two approaches, an indirect approach which is also known as an anti-windup method is the most popular in practice.

Most of the power system controllers usually have saturation limits which are resulted from the safety reason or physical limitation of the equipment rating. These saturation limits restrict the output signal of the damping controller within the range of permissible values which in turn reduce the signal need for the damping of inter-area oscillations and therefore affect the overall closed-loop system performance [23]. In power system literature, very limited research has been directed on the effect of actuator saturation on the power system stability [24–27]. However, these works have not considered the effect of time-delays. If the actuator saturation and time-delays are not properly accounted in the damping controller design, they deteriorate the overall performance of the system and even lead to the system instability.

Motivated by this, in this paper, to compensate the effect of time-delays and actuator saturation, an AWC is designed by using a delay-dependent Lyapunov–Krasovskii functional and generalised sector conditions for the SDC of the FACTS devices. The H_{∞} output feedback controller with a pole-placement approach reported in [28] is considered for the design of SDC. The performance of the proposed controller has been verified in both MATLAB/Simulink and real-time experimentation pursued on OPAL-RT, a real-time digital simulator. The structure of the proposed controller with the power system is shown in Fig. 1. Throughout the paper, the design of an AWC with H_{∞} output feedback control with pole placement is referred to as our proposed controller. The contributions of the paper are as follows:

- This paper deals with the effect of both time-delay and actuator saturation on the damping of inter-area oscillations of the power system.
- To compensate for the effect of both time-delay and actuator saturation, we propose a delay-dependent AWC for SDC of FACTS devices.
- Sufficient conditions for the asymptotic stability of the system are formulated in the form of linear matrix inequalities (LMIs)





based on Lyapunov-Krasovskii functional and generalised sector conditions.

- To compute the AWC gains, these conditions are cast into the following convex optimisation problem: the minimisation of the *L*₂ gain of the disturbance to the system regulated output.
- The effectiveness of the proposed controller is validated on a two-area four-machine power system with a thyristor controlled series capacitor (TCSC) in both MATLAB/Simulink and OPAL-RT real-time digital simulator.

The remaining of the paper is organised as follows. The problem formulation and some related preliminaries are presented in Section 2. In Section 3, an anti-windup synthesis with timedelay is presented by using Lyapunov–Krasovskii functional. In Section 4, a two-area four-machine power system is considered as a case study to validate the effectiveness of the proposed controller and presented simulation results. In Section 5, experimental results by using Opal-RT real-time simulator are presented. Section 6 provides the conclusions.

Notation: Throughout this paper, \Re^n denotes the *n*-dimensional Euclidean space. $\Re^{n \times m}$ is the set of all $n \times m$ real matrices. The superscripts ' and -1 define transpose and the inverse of a matrix, respectively. $P = P' > 0 (\geq 0)$ denotes that *P* is a real symmetric positive-definite (positive semi-definite) matrix. diag{...} denotes a block-diagonal matrix. *I* denotes the identity matrix with appropriate dimensions. The symmetric term in a symmetric matrix is denoted by '*'.

2 Problem formulation and preliminaries

An inter-connected power system consists of various components such as synchronous generators with their excitation systems (IEEE-ST1A), PSSs, and FACTS controllers such as TCSC, SVC, and several loads. The above components are inter-connected through a transmission network. An inter-connected power system with components above is represented by a set of non-linear differential and algebraic equations (DAEs) are presented in [29].

The linearised state-space form of the pre-fault open-loop power system by excluding the damping controller at an equilibrium point is given by

$$\dot{\mathbf{x}}(t) = \mathbf{A}\mathbf{x}(t) + \mathbf{B}_{\omega}\boldsymbol{\omega}(t) + \mathbf{B}\mathbf{u}(t)$$

$$\mathbf{y}(t) = \mathbf{C}_{y}\mathbf{x}(t)$$

$$\mathbf{z}(t) = \mathbf{C}_{z}\mathbf{x}(t) + \mathbf{D}_{z}\mathbf{u}(t)$$
(1)

where $\mathbf{x}(t) \in \mathbb{R}^{n}$, $\mathbf{u}(t) \in \mathbb{R}^{m}$, $\mathbf{y}(t) \in \mathbb{R}^{p}$, $\mathbf{z}(t) \in \mathbb{R}^{n_{z}}$ and $\boldsymbol{\omega}(t) \in \mathbb{R}^{n_{\omega}}$ are the vectors of state variables, control inputs, measured outputs, regulated outputs and exogenous inputs, respectively. *A*, *B*, *C_y*, *C_z* and D_{z} are constant real matrices with appropriate dimension. $\boldsymbol{\omega}(t) \in \mathbb{R}^{n_{\omega}}$ is exogenous input which consists of different disturbances, bounded by some scalar δ with limited energy, i.e. $\boldsymbol{\omega}(t) \in L_{2}$ and it is represented as follows:

$$\|\boldsymbol{\omega}(t)\|_{2}^{2} = \int_{0}^{\infty} \boldsymbol{\omega}'(t) \boldsymbol{\omega}(t) \, \mathrm{d}t \leq \frac{1}{\delta}, \quad 0 \leq \frac{1}{\delta} \leq \infty.$$
 (2)

In low-frequency inter-area oscillation studies, the fast dynamics are not considered. Hence, the full-order model of the system is not necessary to consider in controller design. To simplify and speed up the controller design procedure, it is necessary to reduce the system order. By employing *Schur* model reduction method [30], the full-order model is reduced where only the poorly damped electromechanical modes are obtained. The linearised reduced model (1) can be written as

$$\dot{x}_{p}(t) = A_{p}x_{p}(t) + B_{\omega}\omega(t) + B_{p}u(t)$$

$$y(t) = C_{1}x_{p}(t)$$

$$z(t) = C_{2}x_{p}(t) + D_{2}u(t)$$

$$(3)$$

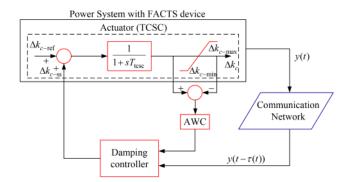


Fig. 1 Block diagram of power system with an anti-windup control

where $x_p(t) \in \Re^{n_p}$ is the vector of reduced-order state variables and A_p , B_p , C_1 , C_2 and D_2 are known constant reduced real matrices with appropriate dimensions.

To enhance the damping of inter-area oscillations of power system described by (3), we consider the following dynamic output feedback controller in the absence of time delays and actuator saturation:

$$\dot{x}_c(t) = A_c x_c(t) + B_c y(t)$$

$$y_c(t) = C_c x_c(t)$$
(4)

where $\mathbf{x}_c \in \mathbf{\mathfrak{R}}^{n_c}$, $\mathbf{y} \in \mathbf{\mathfrak{R}}^{p}$ and $\mathbf{y}_c \in \mathbf{\mathfrak{R}}^{m}$ are the vectors of controller states variable, controller input and controller output, respectively.

In this paper, H_{∞} mixed-sensitivity formulation with robust pole placement approach presented in [28] is considered to design controller (4) to enhance the damping of inter-area oscillation in the absence of time delays and actuator saturation.

Remark 1: For system (3), if there exist $n_p \times n_p$ matrices $\mathbf{R} > 0$, $\mathbf{S} > 0$, $n_c \times n_c$ matrix \hat{A} , $n_c \times p$ matrix \hat{B} and $m \times n_c$ matrix \hat{C} such that, [28]

$$\begin{bmatrix} \boldsymbol{R} & \boldsymbol{I} \\ * & \boldsymbol{S} \end{bmatrix} > 0 \tag{5}$$

$$\begin{bmatrix} \lambda_{ij} \begin{bmatrix} \mathbf{R} & \mathbf{I} \\ * & \mathbf{S} \end{bmatrix} + \mu_{ij} \Phi + \mu_{ji} \Phi' \Big|_{i,j} < 0$$
(6)

$$\begin{bmatrix} \Lambda_{11} & \Lambda'_{21} \\ * & \Lambda_{22} \end{bmatrix} < 0 \tag{7}$$

where

$$\Phi = \begin{bmatrix} A_p R + B_p \hat{C} & A_p \\ \hat{A} & SA_p + \hat{B}C_1 \end{bmatrix}$$
$$\Lambda_{11} = \begin{bmatrix} A_p R + RA'_p + B_p \hat{C} + \hat{C}'B'_p & B_\omega \\ * & -\gamma I \end{bmatrix}$$
$$\Lambda_{21} = \begin{bmatrix} \hat{A} + A'_p & SB_\omega \\ C_2 R + D_2 \hat{C} & 0 \end{bmatrix}$$
$$\Lambda_{22} = \begin{bmatrix} A'_p S + SA_p + \hat{B}C_1 + C'_1 \hat{B}' & C'_2 \\ * & -\gamma I \end{bmatrix}$$

Moreover, matrices M and N are formulated from singular value decomposition (SVD) of I - RS, namely

$$MN' = I - RS.$$
(8)

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Then the parameter of the dynamic output feedback controller in the form of (4) that places the closed-loop poles in the region \mathfrak{D} can be computed as

$$C_c = \hat{C}(M')^{-1}$$
$$B_c = N^{-1}\hat{B}$$
(9)

$$\boldsymbol{A}_{c} = \boldsymbol{N}^{-1}(\boldsymbol{A} - \boldsymbol{N}\boldsymbol{B}\boldsymbol{C}_{1}\boldsymbol{R} - \boldsymbol{S}\boldsymbol{B}_{p}\boldsymbol{C}\boldsymbol{M}' - \boldsymbol{S}\boldsymbol{A}_{p}\boldsymbol{R})(\boldsymbol{M}')^{-1}.$$

Consider a time-varying delay in the feedback loop in Fig. 1. To avoid the effect of time-varying delay, the controller (4) can be rewritten as follows:

$$\dot{x}_c(t) = A_c x_c(t) + B_c y(t - \tau(t))$$

$$y_c(t) = C_c x_c(t)$$
(10)

where $\tau(t)$ denotes the time-varying delay that occurs in the feedback loop. The time-varying delay $\tau(t)$ is a continuous differentiable function assumed to be unknown and satisfies the following condition:

$$0 \le \tau(t) \le h, \quad \dot{\tau}(t) \le d \le 1, \tag{11}$$

where h and d denote upper bound of the time-delay and its rate, respectively.

Consider that the controller output is saturated by following limits:

$$\operatorname{sat}(\mathbf{y}_{c}(t)) = \begin{cases} y_{c_{\max}} & \text{if} & \mathbf{y}_{c} \ge y_{c_{\max}} \\ \mathbf{y}_{c} & \text{if} & y_{c_{\min}} < \mathbf{y}_{c} < y_{c_{\max}} \\ y_{c_{\min}} & \text{if} & \mathbf{y}_{c} \le y_{c_{\min}} \end{cases}$$
(12)

In the presence of actuator saturation, the open-loop system (3) can be expressed as

$$\dot{x}_{p}(t) = A_{p} \mathbf{x}_{p}(t) + B_{\omega} \boldsymbol{\omega}(t) + B_{p} \operatorname{sat}(\mathbf{y}_{c}(t))$$

$$\mathbf{y}(t) = C_{1} \mathbf{x}_{p}(t)$$

$$\mathbf{z}(t) = C_{2} \mathbf{x}_{p}(t) + D_{2} \operatorname{sat}(\mathbf{y}_{c}(t))$$
(13)

where $u(t) = \operatorname{sat}(y_c(t))$

The designed damping controller (4) guarantees the stability of the closed-loop system in the absence of time-delay and actuator saturation. To mitigate the effect of time-delays and actuator saturation simultaneously, the controller is modified by adding an AWC using a dead-zone sector non-linearity as follows:

$$\dot{x}_c(t) = A_c \mathbf{x}_c(t) + B_c y(t - \tau(t)) + E_c (\mathbf{y}_c(t) - \operatorname{sat}(\mathbf{y}_c(t)))$$

$$\mathbf{y}_c(t) = C_c \mathbf{x}_c(t)$$
(14)

where E_c is a static AWC gain, the effect of AWC's takes place only when the saturation occurs.

To mitigate the effects of time-delays and actuator saturation, from (11) and (12), the closed-loop power system becomes

$$\begin{aligned} \dot{x}_p(t) &= A_p \mathbf{x}_p(t) + \mathbf{B}_\omega \omega(t) + \mathbf{B}_p \text{sat}(\mathbf{C}_c \mathbf{x}_c(t)) \\ \dot{x}_c(t) &= A_c \mathbf{x}_c(t) + \mathbf{B}_c \mathbf{C}_1 \mathbf{x}_p(t - \tau(t)) + E_c(\mathbf{C}_c \mathbf{x}_c(t) - \text{sat}(\mathbf{C}_c \mathbf{x}_c(t))) \\ z(t) &= \mathbf{C}_2 \mathbf{x}_p(t) + D_2 \text{sat}(\mathbf{C}_c \mathbf{x}_c(t)) \\ \mathbf{y}_c(t) &= \mathbf{C}_c \mathbf{x}_c(t) \end{aligned}$$
(15)

Define an augmented vector as

$$\boldsymbol{\eta}(t) = \begin{bmatrix} \boldsymbol{x}_p \\ \boldsymbol{x}_c \end{bmatrix} \in \mathfrak{R}^{n_p \times n_c}$$
(16)

and the matrices are given by

$$\boldsymbol{A}_{cl} = \begin{bmatrix} \boldsymbol{A}_p & \boldsymbol{B}_p \boldsymbol{C}_c \\ \boldsymbol{0} & \boldsymbol{A}_c \end{bmatrix}, \quad \boldsymbol{A}_d = \begin{bmatrix} \boldsymbol{0} & \boldsymbol{0} \\ \boldsymbol{B}_c \boldsymbol{C}_1 & \boldsymbol{0} \end{bmatrix}, \quad \boldsymbol{B}_{cl} = \begin{bmatrix} \boldsymbol{B}_p \\ \boldsymbol{0} \end{bmatrix},$$
$$\mathbb{R} = \begin{bmatrix} \boldsymbol{0} \\ \boldsymbol{I}_{n_c} \end{bmatrix}, \quad \boldsymbol{B}_{cl,\omega} = \begin{bmatrix} \boldsymbol{B}_{\omega} \\ \boldsymbol{0} \end{bmatrix}, \quad \boldsymbol{C}_{cl,z} = \begin{bmatrix} \boldsymbol{C}_2 & \boldsymbol{D}_2 \boldsymbol{C}_c \end{bmatrix},$$
$$\boldsymbol{D}_{cl,z} = \begin{bmatrix} \boldsymbol{D}_2 \end{bmatrix}, \quad \boldsymbol{K} = \begin{bmatrix} \boldsymbol{0} & \boldsymbol{C}_c \end{bmatrix}$$

The augmented system can be written as

$$\dot{\eta}(t) = A_{cl}\eta(t) + A_{d}\eta(t - \tau(t)) - (B_{cl} + \mathbb{R}E_{c})\psi(K\eta(t)) + B_{cl,\omega}\omega(t)$$
(18)
$$z(t) = C_{cl,z}\eta(t) + D_{cl,z}\psi(K\eta(t))$$

where $\psi(K\eta(t)) = y_c(t) - \operatorname{sat}(y_c(t)))$ is a dead-zone non-linearity.

Lemma 1: Consider a dead-zone sector condition non-linearity $\psi(K\eta(t))$. If $\eta(t) \in \mathbb{S}$, then $\psi(K\eta(t))$ is always satisfies the following inequality [18]:

$$\psi(\mathbf{K}\boldsymbol{\eta}(t))'Q(\psi(\mathbf{K}\boldsymbol{\eta}(t)) + \mathbf{F}\boldsymbol{\eta}(t)) \leqslant 0, \tag{19}$$

where

$$\mathbb{S} = \boldsymbol{\eta} \in \mathfrak{R}^{n_p + n_c}, \quad y_{c_{\min}} \leqslant (\boldsymbol{K} - \boldsymbol{F}) \boldsymbol{\eta} \leqslant y_{c_{\max}}$$
(20)

is a polyhedral set with a matrix $F \in \Re^{m \times (n_p + n_c)}$, and $Q \in \Re^{m \times m}$ is any positive definite diagonal matrix.

3 Anti-windup synthesis with time-delay

The designed damping controller (4) using Remark 1, guarantees the asymptomatic stability of the closed-loop system in the absence of actuator saturation and time-delays. In this section, we propose an AWC design using delay dependent Lyapunov–Krasovskii functional to compensate the effect of actuator saturation and timedelays simultaneously. The sufficient conditions are formulated in the form of LMIs are given in the following theorem.

Theorem 1: For a given scalars h > 0, d > 0, and $\alpha > 0$, the closed-loop system (18) with AWC gain $E_c = \nu J^{-1}$ is asymptotically stable and possess H_{∞} performance γ for actuator saturation and any time-varying delay $\tau(t)$ satisfying (11) and (12), respectively, if there exist a positive definite symmetric matrices \tilde{X}, \tilde{Y} , and \tilde{Z} , a positive diagonal matrix J and any matrices $P, \tilde{G}, \tilde{H}, W, \nu$ and a positive scalar γ satisfying the following LMIs:

$$\begin{vmatrix} \Omega_{11} & \Omega_{12} & \Omega_{13} & \Omega_{14} & \boldsymbol{B}_{cl,\omega} & \boldsymbol{h}\boldsymbol{G} & \boldsymbol{P}\boldsymbol{C}'_{cl,z} \\ * & \Omega_{22} & \alpha A_d \boldsymbol{P}' & \Omega_{24} & \alpha \boldsymbol{B}_{cl,\omega} & 0 & 0 \\ * & * & \Omega_{33} & 0 & 0 & \boldsymbol{h}\boldsymbol{\bar{H}} & 0 \\ * & * & * & -2\boldsymbol{J} & 0 & 0 & -\boldsymbol{J}\boldsymbol{D}'_{cl,z} \\ * & * & * & * & -\boldsymbol{I} & 0 & 0 \\ * & * & * & * & * & -\boldsymbol{h}\boldsymbol{\bar{Z}} & 0 \\ * & * & * & * & * & * & -\boldsymbol{h}\boldsymbol{\bar{Z}} & 0 \\ * & * & * & * & * & * & -\boldsymbol{\gamma}\boldsymbol{I} \end{vmatrix} < 0, (21)$$

where

$$\begin{split} \Omega_{11} &= \boldsymbol{A}_{cl}\boldsymbol{P}' + \boldsymbol{P}\boldsymbol{A'}_{cl} + \bar{\boldsymbol{Y}} + \bar{\boldsymbol{G}} + \bar{\boldsymbol{G}'}, \ \Omega_{12} &= \bar{\boldsymbol{X}} - \boldsymbol{P}' + \alpha \boldsymbol{P}\boldsymbol{A'}\\ \Omega_{13} &= -\bar{\boldsymbol{G}} + \bar{\boldsymbol{H}'} + \boldsymbol{A}_{d}\boldsymbol{P}', \quad \Omega_{14} &= -(\boldsymbol{B}_{cl}\boldsymbol{J} + \mathbb{R}\boldsymbol{\nu}) + W'\\ \Omega_{22} &= -\alpha \boldsymbol{P}' - \alpha \boldsymbol{P} + h\bar{\boldsymbol{Z}}, \quad \Omega_{24} &= -\alpha(\boldsymbol{B}_{cl}\boldsymbol{J} + \mathbb{R}\boldsymbol{\nu})\\ \Omega_{33} &= -\bar{\boldsymbol{H}} - \bar{\boldsymbol{H}'} - (1 - d)\bar{\boldsymbol{Y}}. \end{split}$$

and

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$$u_{0i} = \min \{y_{c_{\max(i)}}, y_{c_{\min(i)}}\}.$$

Proof: Consider the following Lyapunov candidate function:

$$V(t) = \eta'(t)X\eta(t) + \int_{t-\tau(t)}^{t} \eta'(s)Y\eta(s) \,\mathrm{d}s + \int_{-h}^{0} \int_{t+\theta}^{t} \dot{\eta}'(s)Z\dot{\eta}(s) \,\mathrm{d}s \,\mathrm{d}\theta$$
(23)

where X, Y and Z are positive definite symmetric matrices which are to be determined.

The time derivative of V(t) along the trajectories of the closed-loop system (18) is given by

$$\dot{V}(t) = \dot{\eta}'(t)X\eta(t) + \eta'(t)X\dot{\eta}(t) + \eta'(t)Y\eta(t) - (1-d)\eta'(t-\tau(t))Y\eta(t-\tau(t)) + h\dot{\eta}'(t)Z\dot{\eta}(t) - \int_{t-h}^{t} \dot{\eta}'(s)Z\dot{\eta}(s)$$
(24)

From (11), it is clear that the following is true:

$$-\int_{t-\hbar}^{t} \dot{\eta}'(s) \mathbf{Z} \dot{\eta}(s) \, \mathrm{d}s \le -\int_{t-\tau(t)}^{t} \dot{\eta}'(s) \mathbf{Z} \dot{\eta}(s) \, \mathrm{d}s \tag{25}$$

By using (25), we can rewrite $\dot{V}(\hat{x}_r(t))$ as follows

$$\dot{V}(t) \leq \dot{\eta}'(t)X\eta(t) + \eta'(t)X\dot{\eta}(t) + \eta'(t)Y\eta(t) - (1-d)\eta'(t-\tau(t))Y\eta(t-\tau(t)) + h\dot{\eta}'(t)Z\dot{\eta}(t) - \int_{t-\tau(t)}^{t} \dot{\eta}'(s)Z\dot{\eta}(s) \,\mathrm{d}s$$
(26)

By using Lemma 2.1 from [31], we can expand $\int_{t-\tau(t)}^{t} \dot{\eta}'(s) Z \dot{\eta}(s)$ as follows:

$$-\int_{t-\tau(t)}^{t} \dot{\eta}'(s) \mathbf{Z} \dot{\eta}(s) \leq 2[\eta'(t) \quad \eta'(t-\tau(t))] \begin{bmatrix} \mathbf{G} & -\mathbf{G} \\ \mathbf{H} & -\mathbf{H} \end{bmatrix}$$
$$\times \begin{bmatrix} \boldsymbol{\eta}(t) \\ \eta(t-\tau(t)) \end{bmatrix} + h[\eta'(t) \quad \eta'(t-\tau(t))] \qquad (27)$$
$$\times \begin{bmatrix} \mathbf{G} \\ \mathbf{H} \end{bmatrix} \mathbf{Z}^{-1} \begin{bmatrix} \mathbf{G}' & \mathbf{H}' \end{bmatrix} \begin{bmatrix} \boldsymbol{\eta}(t) \\ \eta(t-\tau(t)) \end{bmatrix}$$

By using sector condition from Lemma 1, the time derivative of V(t) is extended to

$$\dot{V}(t) \le \dot{V}(t) - 2\psi(\mathbf{K}\eta(t))'\mathbf{Q}(\psi(\mathbf{K}\eta(t)) - \mathbf{F}\eta(t))$$
(28)

By using free-weighting matrix method [32], the following relation holds for the appropriately sized matrices P_1 , P_2 :

$$0 = 2[\eta'(t)\boldsymbol{P}_1 + \dot{\eta}'(t)\boldsymbol{P}_2] \times [-\dot{\eta}(t) + A_{cl}\eta(t) + A_d\eta(t - \tau(t)) - (B_{cl} + \mathbb{R}E_c)\psi(K\eta(t)) + B_{cl,\omega}\omega(t)]$$
(29)

Then, adding the terms on the right of (27) and (29) into (28), we have

$$\begin{split} \dot{V}(t) &\leq \dot{\eta}'(t) X \eta(t) + \eta'(t) X \dot{\eta}(t) \\ &+ \eta'(t) Y \eta(t) - (1 - d) \eta'(t - \tau(t)) Y \eta(t - \tau(t)) \\ &+ h \dot{\eta}'(t) Z \dot{\eta}(t) + 2 [\eta'(t) \quad \eta'(t - \tau(t))] \begin{bmatrix} \boldsymbol{G} & -\boldsymbol{G} \\ \boldsymbol{H} & -\boldsymbol{H} \end{bmatrix} \\ &\times \begin{bmatrix} \boldsymbol{\eta}(t) \\ \eta(t - \tau(t)) \end{bmatrix} + h [\eta'(t) \quad \eta'(t - \tau(t))] \begin{bmatrix} \boldsymbol{G} \\ \boldsymbol{H} \end{bmatrix} Z^{-1} [\boldsymbol{G}' \quad \boldsymbol{H}'] \quad (30) \\ &\times \begin{bmatrix} \boldsymbol{\eta}(t) \\ \eta(t - \tau(t)) \end{bmatrix} + 2 [\eta'(t) \boldsymbol{P}_1 + \dot{\eta}'(t) \boldsymbol{P}_2] \times [-\dot{\eta}(t) + \boldsymbol{A}_{cl} \eta(t) \\ &+ \boldsymbol{A}_{d} \eta(t - \tau(t)) - (\boldsymbol{B}_{cl} + \mathbb{R} E_{c}) \psi(\boldsymbol{K} \eta(t)) + \boldsymbol{B}_{cl,\omega} \boldsymbol{\omega}(t)] \\ &- 2 \psi(\boldsymbol{K} \eta(t))' \boldsymbol{Q}(\psi(\boldsymbol{K} \eta(t)) - \boldsymbol{F} \eta(t)) \end{split}$$

Define an auxiliary function for the H_{∞} performance of the system (18)

$$M(t) = \dot{V}(t) - \omega'(t)\boldsymbol{\omega}(t) + \frac{1}{\gamma}z'(t)z(t)$$
(31)

and it can be simplified as follows:

$$M(t) \leq \xi'(t)\Lambda\xi(t) + h[\eta'(t) \quad \eta'(t-\tau(t))] \begin{bmatrix} \boldsymbol{G} \\ \boldsymbol{H} \end{bmatrix} Z^{-1}$$

$$\times \begin{bmatrix} \boldsymbol{G}' & \boldsymbol{H}' \end{bmatrix} \begin{bmatrix} \boldsymbol{\eta}(t) \\ \boldsymbol{\eta}(t-\tau(t)) \end{bmatrix} + \begin{bmatrix} \eta'(t) & \boldsymbol{\psi}(\boldsymbol{K}\boldsymbol{\eta}(t))' \end{bmatrix} \quad (32)$$

$$\times \begin{bmatrix} \boldsymbol{C}'_{cl,z} \\ -\boldsymbol{D}'_{cl,z} \end{bmatrix} \frac{1}{\gamma} \begin{bmatrix} \boldsymbol{C}_{cl,z} & -\boldsymbol{D}_{cl,z} \end{bmatrix} \begin{bmatrix} \boldsymbol{\eta}(t) \\ \boldsymbol{\psi}(\boldsymbol{K}\boldsymbol{\eta}(t)) \end{bmatrix}$$

where

$$\Lambda = \begin{bmatrix} \Lambda_{11} & \Lambda_{12} & \Lambda_{13} & \Lambda_{14} & P_1 B_{cl,\omega} \\ * & \Lambda_{22} & P_2 A_d & -P_2 (B_{cl} + \mathbb{R} E_c) & P_2 B_{cl,\omega} \\ * & * & \Lambda_{33} & 0 & 0 \\ * & * & * & -2Q & 0 \\ * & * & * & * & -I \end{bmatrix}$$

$$\Lambda_{11} = \mathbf{P}_{1}\mathbf{A}_{cl} + A'_{cl}\mathbf{P}'_{1} + \mathbf{Y} + \mathbf{G} + \mathbf{G}', \quad \Lambda_{12} = \mathbf{X} - \mathbf{P}_{1} + A'_{cl}\mathbf{P}'_{2}$$

$$\Lambda_{13} = -\mathbf{G} + H' + \mathbf{P}_{1}A_{d}, \quad \Lambda_{14} = -\mathbf{P}_{1}(\mathbf{B}_{cl} + \mathbb{R}E_{c}) + F'\mathbf{Q}$$

$$\Lambda_{22} = -\mathbf{P}_{2} - \mathbf{P}'_{2} + h\mathbf{Z}, \quad \Lambda_{33} = -H - H' - (1 - d)\mathbf{Y}$$

with

$$\xi'(t) = \begin{bmatrix} \eta'(t) & \dot{\eta}'(t) & \eta'(t-\tau(t)) & \psi(\boldsymbol{K}\boldsymbol{\eta}(t))' & \omega'(t) \end{bmatrix}$$

Using Schur complement [33], the inequality (32) is equal to

$$M(t) \le \xi'(t)\Psi\xi(t) \tag{33}$$

where

$$\Psi = \begin{bmatrix} \Psi_{11} & \Psi_{12} & \Psi_{13} & \Psi_{14} & P_1 B_{cl,\omega} & hG & C'_{cl,z} \\ * & \Psi_{22} & P_2 A_d & \Psi_{24} & P_2 B_{cl,\omega} & 0 & 0 \\ * & * & \Psi_{33} & 0 & 0 & hH & 0 \\ * & * & * & -2Q & 0 & 0 & -D'_{cl,z} \\ * & * & * & * & -I & 0 & 0 \\ * & * & * & * & * & -hZ & 0 \\ * & * & * & * & * & * & -\gamma I \end{bmatrix}$$

$$\Psi_{11} = \mathbf{P}_{1}\mathbf{A}_{cl} + A'_{cl}P'_{1} + \mathbf{Y} + \mathbf{G} + \mathbf{G}', \quad \Psi_{12} = \mathbf{X} - \mathbf{P}_{1} + A'_{cl}P'_{2}$$

$$\Psi_{13} = -\mathbf{G} + H' + \mathbf{P}_{1}A_{d}, \quad \Psi_{14} = -\mathbf{P}_{1}(\mathbf{B}_{cl} + \mathbb{R}E_{c}) + F'\mathbf{Q}$$

$$\Psi_{22} = -\mathbf{P}_{2} - P'_{2} + h\mathbf{Z}, \quad \Psi_{24} = -\mathbf{P}_{2}(\mathbf{B}_{cl} + \mathbb{R}E_{c})$$

$$\Psi_{33} = -H - H' - (1 - d)\mathbf{Y}$$

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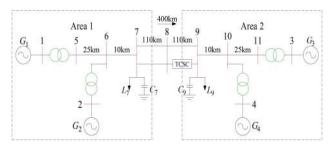


Fig. 2 Line diagram of two-area four-machine system

Table 1	Inter- and local-area modes of two-area four-machine system	

Modes	Inter		Local 1		Local 2	
	ζ	<i>f</i> , Hz	ζ	<i>f</i> , Hz	ζ	<i>f</i> , Hz
WC	0.0717	0.6534	0.1752	1.2186	0.1684	1.2686
HwoDS	0.1567	0.6371	0.1751	1.2194	0.1674	1.2700
PC	0.3283	0.6450	0.1721	1.2191	0.1668	1.2686

WC – without controller, HwoDS – H_{∞} controller without delays and saturation, PC – proposed controller.

From (33), if $\Psi < 0$, then the auxiliary function M(t) is negative definite, which ensures that the closed-loop system is asymptotically stable.

To solve an AWC gain E_c , introduce some change of variables as

$$P = P_1^{-1}, \ \bar{X} = PXP', \ \bar{Y} = PYP', \ \bar{Z} = PZP$$
$$\bar{G} = PGP', \ \bar{H} = PHP', \ J = Q^{-1},$$
$$W = FP', \ \nu = E_c O^{-1}$$

Considering $P_2 = \alpha P_1$, where α is a scalar tuning parameter and pre- and post-multiply the inequality $\Psi < 0$ by Π and Π' , respectively, with $\Pi = \text{diag}\{P_1^{-1}, P_1^{-1}, P_1^{-1}, Q^{-1}, I, P_1^{-1}, I\}$, i.e.

$$\Pi \Psi \Pi' < 0 \tag{34}$$

From inequality (34), we obtain the inequality (21).

To include actuator saturation in the controller design, we consider a sector constraint as follows:

$$(K_i - F_i)'(K_i - F_i) \le u_{0i}^2 X, \quad i = 1, ..., m.$$
 (35)

By using Schur complement, we can rewrite (35) as

$$\begin{bmatrix} X & K'_i - F'_i \\ u_{0i}^2 \end{bmatrix} \ge 0, \quad i = 1, ..., m.$$
(36)

Pre- and post-multiply inequality (36) by

$$\begin{bmatrix} \boldsymbol{P} & \boldsymbol{0} \\ \boldsymbol{0} & \boldsymbol{I} \end{bmatrix} \text{ and } \begin{bmatrix} \boldsymbol{P}' & \boldsymbol{0} \\ \boldsymbol{0} & \boldsymbol{I} \end{bmatrix},$$

respectively and we get the inequality (22), which concludes proof of the theorem. \square

4 Case study: a two-area four-machine power system

4.1 System description

The benchmark model of the two-area four-machine power system is considered to validate the efficiency of the proposed controller as shown in Fig. 2, which is widely used for studying the lowfrequency oscillations. The system consists of two areas and each area consists of two generators and five buses. These two areas are weakly connected by tie-line buses, so there are total of 11 buses and 4 generators in the whole system. The synchronous generators $G_i(i = 1, 2, 3, 4)$ are represented by a sixth-order sub-transient model with state variables δ , ω , E'_d , E'_q , ψ_{1d} , ψ_{2q} , which are equipped with a simple, fast acting first-order IEEE-ST1A type static excitation system. To damp out the local area oscillations G_1 and G_3 are equipped with a third-order PSS. The line data, bus data and the parameters of generators, PSS, and exciters are given in [34].

In normal operating condition, active power flows from Area 1 to Area 2 is ~400 MW. To improve the power transfer capability and enhance the damping of inter-area oscillations, a series FACTS device, i.e. a TCSC is connected in series with the transmission line #8-#9. The initial percentage compensation $\% k_c$ of TCSC is considered 10% with 50 and 1% as their upper and lower limits in the presence of supplementary damping controller.

4.2 Modal analysis and model-order reduction

From eigenvalue analysis, three electromechanical modes of oscillations are presented in the four-machine two-area system as shown in Table 1. Out of these three modes, two are local modes and one is inter-area mode. Modal controllability and observability [34] have been used to select a suitable controller placement and feedback signals for damping controller design. A TCSC connected at line #8-#9 is taken the controller location and active power deviation of line #10-#9 (ΔP_{10-9}) is taken as a feedback signal, these having higher controllability and observability corresponding to the critical oscillatory mode.

By considering the control signal of TCSC ($(\% k_c)$ as the input and ΔP_{10-9} as a feedback output signal, the power system model is linearised at the equilibrium point. The size of the linearised model is 34th order resulting in complexity in the controller design. To make the controller design convenient and feasible, model order reduction is required. By using *Schur* balanced model reduction method [30], the original system is reduced to 8th order. The reduced-order model will remain the information about the poorly damped oscillatory modes. The frequency responses of both full and reduced-order model are shown in Fig. 3. From the frequency response, 8th-order reduced model is used for the design of the proposed controller.

4.3 Results and discussion

The reduced 8th-order linearised open-loop power system model (3) is used to design the proposed damping controller.

First of all, the SDC of TCSC device is designed for the reduced model (3) by using H_{∞} output feedback control with pole placement approach presented in Remark 1, without considering the effects of actuator saturation and time-delays. For H_{∞} output feedback control, the frequency dependent weighting matrices are chosen as follows:

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$$W_1 = \frac{30}{s+30}, \quad W_2 = \frac{10s}{s+100}.$$
 (37)

To ensure the minimum damping ratio of 0.15 for the closed-loop system, a conic sector region \mathfrak{D} with an inner angle $2\cos^{-1}0.15$ with the apex at the origin is selected for the pole-placement region. The order of the designed controller from Remark 1 is equal to the order of reduced model plus order of the weights. The 10th order of the controller is again reduced to 8th order by using *Schur* balanced model reduction method.

Simulation studies are carried out on a non-linear power system model in MATLAB/Simulink to evaluate the effectiveness of the H_{∞} output feedback controller designed without considering timedelays and actuator saturation. A three-phase to ground fault is considered as a disturbance on the power system which takes place in a transmission line #8-#9 at 1 s with duration of 100 ms at 400 MW tie-line active power flow as an attempt to excite the interarea mode. The response of the speed deviation of $G_1 - G_3$ and percentage compensation of TCSC ($\%k_c$) is depicted in Figs. 4 and 5 to show the effect of actuator saturation and time-delays on the damping of inter-area oscillations and therefore effect on the closed-loop performance when the controller is designed without considering time-delays and actuator saturation.

4.3.1 Effect of actuator saturation: The effect of actuator saturation on the closed-loop performance (regarding speed deviation of $G_1 - G_3$ and percentage compensation of TCSC ($(\%k_c)$) is depicted in Fig. 4. From Fig. 4, we can observe that when the actuator output is limited by minimum and maximum values there will be saturation occurs in the response of $\%k_c$ of TCSC and due

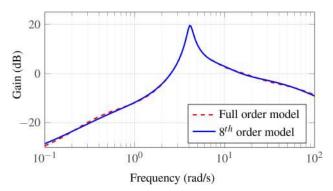


Fig. 3 Frequency response of the full-order versus the reduced-order system

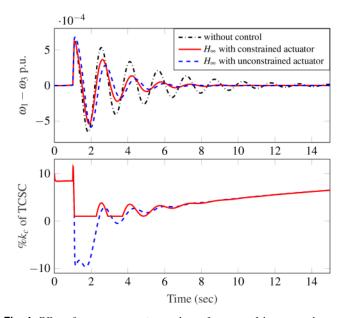


Fig. 4 Effect of actuator saturation on the performance of the case study

to saturation the damping of the oscillation mode is deteriorated as indicated in the transient response of the speed deviation of $G_1 - G_3$. For the comparison purpose, the transient response of the speed deviation of $G_1 - G_3$ and percentage compensation of TCSC ($\% k_c$) are also shown in Fig. 4 when the actuator output is not limited by minimum and maximum values in the simulation. The comparison of responses of the speed deviation with and without constrained actuator confirms that the damping predicted in the controller design is not achieved due to the actuator saturation presence. The limits of the actuator are unavoidable due to the physical and security limitation of the actuator. So to mitigate the effect of actuator saturation, it is necessary to consider the actuator saturation constraints in the design procedure of the controller. In this work, an add-on AWC is designed for the H_{∞} output feedback controller to minimise the effect caused by the actuator saturation.

4.3.2 Effect of time-delay: The effect of time-delay on the closed-loop performance is depicted in Fig. 5 when H_{∞} output feedback controller designed without considering time-delays and actuator saturation. From Fig. 5, we can observe that the H_{∞} output feedback controller exhibits satisfactory results to damp the interarea oscillations when there is no delay (i.e. h = 0 ms) in the feedback signal. But when the time delay occurs and increases, the damping of the inter-area mode is deteriorated and the controller is incapable of providing satisfactory performance of the closed-loop system. When the time-delay increase, the control signal is saturating more and that is degrading the performance of the closed-loop system. The responses in Fig. 5 prove that the timedelays in the feedback loop will degrade the performance and even lead to instability of the closed-loop power system. To compensate the effect of time-delays, it is necessary to consider the time-delays in the controller design.

To overcome actuator saturation and time-delay problems simultaneously, we propose a delay-dependent AWC by using Lyapunov–Krasovskii functional.

To compute an AWC gain E_c , the proposed problem is converted into a convex optimisation problem, which ensures the stability of the closed-loop system (18) for time-delays and saturation satisfying (11) and (12), respectively. To minimise the upper bound for L_2 a gain of $\omega(t)$ on z(t), consider the following convex optimisation problem

$$\begin{array}{c} \min \ \gamma \\ \text{s.t. LMIs} \ (21) \ \text{and} \ (22). \end{array}$$
(38)

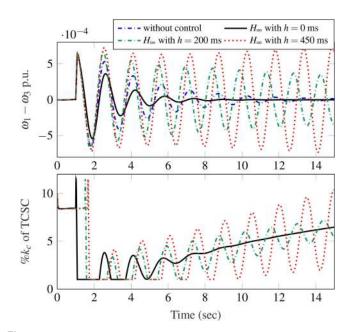


Fig. 5 Effect of time delays on the performance of the case study

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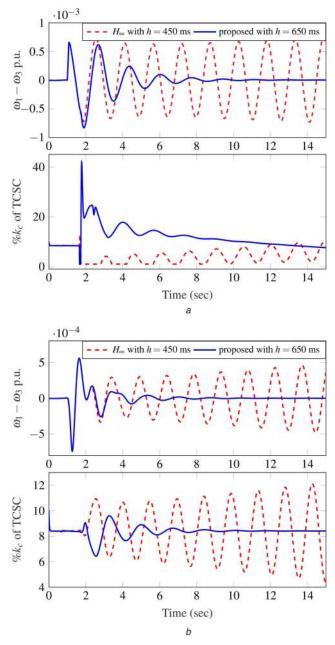


Fig. 6 *MATLAB simulation responses of case study for scenarios 1 and 2 with proposed controller compared with* H_{∞} *controller*

(a) Dynamic response of case study for scenario 1, (b) Dynamic response of case study for scenario 2

The optimisation problem (38) is solved for the closed-loop system (18) by using LMI toolbox in MATLAB with d = 0.1, $\alpha = 0.7$, $y_{c_{max}} = 0.5$, $y_{c_{min}} = 0.01$, and for delay margin h = 650ms, we obtain $\gamma = 55.09$ and an AWC gain

$$E_c = \begin{bmatrix} 16.4929 & 2.1922 & -9.8625 & 0.5598 \\ 17.9987 & 0.7247 & 2.8049 & 0.7848 \end{bmatrix}^{\mathrm{T}}$$
(39)

The efficiency of the proposed damping controller is verified by the following two scenarios.

Scenario 1: A three-phase to ground fault takes place on a transmission line #8-#9 at 1 s with a duration of 100 ms. Scenario 2: The generator G_1 terminal voltage increases +5% step at 1 s.

The responses of the rotor speed deviation of the generator 1 and generator 3 ($\omega_1 - \omega_3$) and percentage compensation of TCSC ($(\%k_c)$ of a case study with the proposed controller, compared with

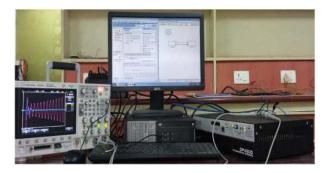


Fig. 7 Experimental set-up of OPAL-RT OP4500 simulator

 H_{∞} output feedback control with pole placement are shown in Fig. 6 for scenarios 1 and 2. From dynamic responses shown in Fig. 6, it can be observed that the H_{∞} output feedback controller designed without considering time-varying delays and actuator saturation provides less damping to inter-area oscillations when the time-delay h = 450 ms occurs in the feedback loop and the control signal is saturated by reaches its maximum value. However, the proposed controller successfully damps out the inter-area low-frequency oscillation even when the time-delay h = 650 ms occurs by providing sufficient damping under different operating conditions. From the response of TCSC output, it is observed that the H_{∞} output feedback controller exhibits actuator saturation at different operating conditions, but the proposed controller reduces the saturation region and improves the dynamic performance of the closed-loop system.

5 Real-time simulation (RTS) results

RTS is an integral part of research and development of power systems and other engineering fields [35]. RTSs are performed in OPAL-RT OP4500 real-time digital simulator using INTEL multicore processors with Xilinx Kintex 7 FPGA MMPK7 board. OPAL-RT provides a distributed real-time platform and it allows to model and simulates the power system in MATLAB/Simulink with high accuracy, low cost and small time-steps to precisely emulate the actual power system. The efficiency of the proposed controller is demonstrated in real-time fewer than two different scenarios of the case study. The experimental set-up of OPAL-RT OP4500 simulator is shown in Fig. 7. The detailed non-linear power system with damping controller is modelled in MATLAB/Simulink and further editing is performed to make it to compatible with Opal-RT by using its RT-LAB library. Then, the model is split into two subsystems, e.g. master subsystem, where plant and controller are kept and console subsystem, where outputs are displayed. The total split model is loaded into the Opal-RT server to convert it to C code. By setting solver time-step as fixed time-step mode with time-step of 0.8 ms, the program is executed. The results obtained from the simulator are shown in Fig. 8. From the RTSs for both operating conditions, it can be observed that the proposed damping controller guarantees the performance and stability of closed-loop wide-area power system with time-delays and actuator saturation in real-time

6 Conclusions

The problem of time-delays with actuator saturation and its influence on the performance and stability of a closed-loop system has been studied for years, but the control approaches for power systems with actuator saturation have been rarely investigated. The power system actuators are subjected to the limited magnitude and the time-delays are inevitable in the feedback loop. If these constraints are not considered in the design procedure of the controller, the damping performance of the controller maybe degraded or even lead to the instability of the closed-loop power system. The main contribution of this work is to design a delaydependent AWC for the power system with time-delays and actuator saturation to enhance the damping of inter-area oscillations. Sufficient conditions required for the asymptotic stability of the closed-loop power system are derived by using

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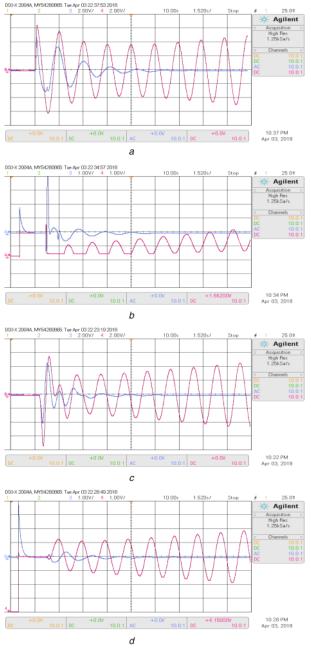


Fig. 8 RTS responses of case study for scenarios 1 and 2 with proposed controller (h = 650 ms) compared with H_{∞} controller (h = 450 ms) (a) Speed deviation response of $\omega_1 - \omega_3$ of case study for scenario 1, (b) TSCS output response of case study for scenario 1, (c) Speed deviation response of $\omega_1 - \omega_3$ of case study for scenario 2, (d) TCSC output response of case study for scenario 2

Lyapunov-Krasovskii functional and generalised sector condition. These conditions are then formulated as an LMI convex optimisation, allowing the proposed controller to minimise the L_2 gain of the disturbance $\omega(t)$ to the system regulated output z(t).

The efficiency of the proposed controller is validated by implementing the case study namely a two-area four-machine power system in both MATLAB/Simulink and OPAL-RT real-time simulator. From results obtained from both MATLAB/Simulink and real-time simulator, it clearly shows that the H_{∞} output feedback controller not provides sufficient damping to the interarea oscillations when the time-delay occur in the feedback and the control signals subjected to actuator saturation. However, the proposed controller exhibits excellent performance in the presence of time-delays and actuator saturation and provides sufficient damping to the inter-area oscillations as compared to H_{∞} output feedback controller.

7 References

- [1] Rogers, G.: 'Power system oscillations' (Springer Science & Business Media, New York, 2012)
- Wu, H., Tsakalis, K.S., Heydt, G.T.: 'Evaluation of time delay effects to wide-[2] area power system stabilizer design', IEEE Trans. Power Syst., 2004, 19, (4), pp. 1935-1941
- [3] Chompoobutrgool, Y., Vanfretti, L., Ghandhari, M.: 'Survey on power system stabilizers control and their prospective applications for power system damping using synchrophasor-based wide-area systems', Int. Trans. Electr. Energy Syst., 2011, 21, (8), pp. 2098–2111
- Shakarami, M., Davoudkhani, I.F.: 'Wide-area power system stabilizer design [4] based on grey wolf optimization algorithm considering the time delay', Electr. Power Syst. Res., 2016, 133, pp. 149-159
- Li, Y., Rehtanz, C., Ruberg, S., et al.: 'Wide-area robust coordination [5] approach of HVDC and FACTS controllers for damping multiple interarea oscillations', IEEE Trans. Power Deliv., 2012, 27, (3), pp. 1096-1105
- [6] Yao, W., Jiang, L., Wen, J., et al.: 'Wide-area damping controller of FACTS devices for inter-area oscillations considering communication time delays', *IEEE Trans. Power Syst.*, 2014, **29**, (1), pp. 318–329
- [7] Deng, J., Li, C., Zhang, X.P.: 'Coordinated design of multiple robust FACTS damping controllers: a BMI-based sequential approach with multi-model
- systems', *IEEE Trans. Power Syst.*, 2015, **30**, (6), pp. 3150–3159 Kamwa, I., Grondin, R., Hébert, Y.: 'Wide-area measurement based stabilizing control of large power systems-a decentralized/hierarchical approach', *IEEE Trans. Power Syst.*, 2001, **16**, (1), pp. 136–153 Zhang, W., Branicky, M.S., Phillips, S.M.: 'Stability of networked control [8]
- [9] systems', IEEE Control Syst., 2001, 21, (1), pp. 84-99
- [10] Li, Y., Zhou, Y., Liu, F., et al.: 'Design and implementation of delaydependent wide-area damping control for stability enhancement of power systems', IEEE Trans. Smart Grid, 2017, 8, (4), pp. 1831–1842 Singh, A.K., Singh, R., Pal, B.C.: 'Stability analysis of networked control in
- [11] Wang, S., Meng, X., Chen, T.: 'Wide-area control of power systems through
- [12] delayed network communication', IEEE Trans. Control Syst. Technol., 2012, 20, (2), pp. 495-503
- [13] Molina-Cabrera, A., Ros, M.A., Besanger, Y., et al.: 'A latencies tolerant model predictive control approach to damp inter-area oscillations in delayed power systems', *Int. J. Electr. Power Energy Syst.*, 2018, **98**, pp. 199–208 Fan, R., Huang, R., Sun, L.: 'Nonlinear model predictive control of HVDC
- [14] for inter-area oscillation damping', Electr. Power Syst. Res., 2018, 165, pp. 27 - 34
- [15] Azad, S.P., Iravani, R., Tate, J.E.: 'Damping inter-area oscillations based on a model predictive control (MPC) HVDC supplementary controller', *IEEE Trans. Power Syst.*, 2013, **28**, (3), pp. 3174–3183 Jain, A., Biyik, E., Chakrabortty, A.: 'A model predictive control design for
- [16] selective modal damping in power systems'. IEEE American Control Conf. (ACC), Chicago, July 2015, pp. 4314-4319
- [17] Hu, T., Lin, Z.: 'Control systems with actuator saturation: analysis and design' (Springer Science & Business Media, New York, 2001)
- Tarbouriech, S., Garcia, G., da Silva, J.M.G.Jr., et al.: 'Stability and [18] stabilization of linear systems with saturating actuators' (Springer Science & Business Media, London, 2011)
- Dang, Q.V., Vermeiren, L., Dequidt, A., et al.: 'Robust stabilizing controller design for Takagi–Sugeno fuzzy descriptor systems under state constraints [19] and actuator saturation', Fuzzy Sets Syst., 2017, PP, (99), pp. 1-1
- [20] Hu, T., Teel, A.R., Zaccarian, L.: 'Stability and performance for saturated systems via quadratic and nonquadratic Lyapunov functions', IEEE Trans. Autom. Control, 2006, **51**, (11), pp. 1770–1786 Lu, L., Lin, Z.: 'Design of switched linear systems in the presence of actuator
- [21] saturation', IEEE Trans. Autom. Control, 2008, 53, (6), pp. 1536-1542
- Zhang, X.Q., Zhao, J.: 'L2-gain analysis and anti-windup design of discrete-[22] time switched systems with actuator saturation'. Int. J. Autom. Comput., 2012. 9, (4), pp. 369–377
- Nguyen, T., Gianto, R.: 'Optimal design for control coordination of power [23] system stabilisers and flexible alternating current transmission system devices with controller saturation limits', IET Gener. Transm. Distrib., 2010, 4, (9), pp. 1028-1043
- Xin, H., Gan, D., Qu, Z., et al.: 'Impact of saturation nonlinearities/ [24] disturbances on the small-signal stability of power systems: an analytical approach', Electr. Power Syst. Res., 2008, 78, (5), pp. 849-860
- Fang, J., Yao, W., Chen, Z., et al.: 'Design of anti-windup compensator for [25] energy storage-based damping controller to enhance power system stability',
- IEEE Trans. Power Syst., 2014, **29**, (3), pp. 1175–1185 Soliman, H.M., Yousef, H.A.: 'Saturated robust power system stabilizers', *Int. J. Electr. Power Energy Syst.*, 2015, **73**, pp. 608–614 [26]
- Raoufat, M.E., Tomsovic, K., Djouadi, S.M.: 'Power system supplementary [27] damping controllers in the presence of saturation'. IEEE Power and Energy Conf. (PECI), Illinois, 2017, pp. 1-6
- Gahinet, P.: 'H_∞ design with pole placement constraints: an LMI approach', [28] IEEE Trans. Autom. Control, 1996, 45, (3), pp. 358-367
- [29] Pal, B., Chaudhuri, B.: 'Robust control in power system' (Springer, New York, U.S.A, 2005)
- [30] Chiang, M.G., Safonov, R.Y.: 'A Schur method for balanced model
- reduction', *IEEE Trans. Autom. Control*, 1989, **AC-34**, pp. 729–733 El-Haoussi, F., Tissir, E.H., Tadeo, F., *et al.*: 'Delay-dependent stabilisation of systems with time-delayed state and control: application to a quadruple-tank [31] process', Int. J. Syst. Sci., 2011, 42, (1), pp. 41-49
- [32] He, Y., Wu, M., She, J.H., et al.: 'Parameter-dependent Lyapunov functional for stability of time-delay systems with polytopic-type uncertainties', IEEE Trans. Autom. Control, 2004, 49, (5), pp. 828-832

IET Smart Grid, 2019, Vol. 2 Iss. 1, pp. 106-114

- [33]
- Boyd, S., El-Ghaoui, L., Feron, E., et al.: 'Linear matrix inequalities in system and control theory' (SIAM, Philadelphia, 1994) Kundur, P., Balu, N.J., Lauby, M.G.: 'Power system stability and control' (McGraw-Hill, New York, 1994) [34]
- [35] Jiang, Z., Konstantinou, G., Zhong, Z., et al.: 'Real-time digital simulation based laboratory test-bench development for research and education on solar PV systems'. IEEE Universities Power Engineering Conf. (AUPEC), Australasian, 2017, pp. 1–6